WINSTAR Display

OLED SPECIFICATION

Model No:

WEO012864AA

General Specification

Item	Dimension	Unit		
Dot Matrix	128 x 64	- /		
Module dimension	42.04 x 27.22 x 1.41	mm		
Active Area	35.05 x 17.51	mm		
Pixel Size	0.249 x 0.249	mm		
Pixel Pitch	0.274 x 0.274	mm		
Display Mode	Passive Matrix			
Display Color	Monochrome			
Drive Duty	1/64 Duty			
IC	SSD1327			
Interface	6800,8080, 4-wire SPI,I2C			
Size	1.54 inch			

WINSTAR

Contour Drawing & Block Diagram



Interface Pin Function

Pin No.	Symbol	Description				
1	VSS	This is a ground pin.				
2	VCC	Power supply for panel driving voltage.				
3	VCOMH	COM signal deselected voltage level. A capacitor should be connected between this pin and VSS. No external power supply is allowed to connect to this pin.				
4	VCI	Low voltage power supply and power supply for interface logic level. It should match with the MCU interface voltage level and must be connected to external source. VCI must always set to be equivalent to or higher than VDD.				
5	VDD	Power supply pin for core logic operation. VDD can be supplied externally (within the range of 2.4V to 2.6V) or regulated internally from VCI. A capacitor should be connected between VDD and VSS under all circumstances.				
6	BS1	Communicating Protocol Select These pins are MCU interface selection input. See the following table:				
7	BS2	BS1 0 1 0 1 BS2 1 1 0 0				
8	VSS	This is a ground pin.				
9	IREF	This pin is the segment output current reference pin. A resistor should be connected between this pin and VSS to maintain the current around 10uA.				
10	CS#	This pin is the chip select input. The chip is enabled for MCU communication only when CS# is pulled low.				
11	RES#	This pin is reset signal input. When the pin is low, initialization of the chip is executed.				
12	D/C#	This pin is Data/Command control pin connecting to the MCU. When the pin is pulled HIGH, the data at D[7:0] will be interpreted as data. When the pin is pulled LOW, the data at D[7:0] will be transferred to a command register. In I2C mode, this pin acts as SA0 for slave address selection.				
13	R/W# (WR#)	This pin is read / write control input pin connecting to the MCU interface. When 6800 interface mode is selected, this pin will be used as Read/Write (R/W#) selection input. Read mode will be carried out when this pin is pulled HIGH and write mode when LOW. When 8080 interface mode is selected, this pin will be the Write (WR#) input Data write operation is initiated when this pin is pulled LOW and the chip is selected. When serial or I2C interface is selected, this pin must be connected to VSS				

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Low voltage power supply, power supply for I/O pins	VCI	-0.3	4.0	v
Supply Voltage for Logic	VDD	-0.5	2.75	V
Supply Voltage for Display	VCC	-0.5	19.0	V
Operating Temperature	TOP	-40	+80	°C
Storage Temperature	TSTG	-40	+85	°C

Electrical Characteristics

DC Electrical Characteristics

Item	Symbol	Condition	Min	Тур	Max	Unit
Low voltage power supply, power supply for I/O pins	VCI	S,	1.65	3.0	3.5	V
Supply Voltage for Display	VCC	· · · ·	8.0	12.5	13.0	V
Supply Voltage for Logic	VDD	_	1.65	_	2.6	V
High Level Input	VIH	_	0.8×VCI		VCI	V
Low Level Input	VIL	_	0		0.2×VCI	V
High Level Output	VOH	_	0.9×VCI		VCI	V
Low Level Output	VOL	—	0		0.1×VCI	V
50% Check Board operating Current	ICC	VCC =12.5V		10	20	mA