



Features

- 600V, 5A, Low $V_{CE(sat)}$
- Trench-Gate Field-Stop technology
- Optimized for conduction
- Robust
- RoHS compliant*

Applications

- Switch-Mode Power Supplies (SMPS)
- Uninterruptible Power Sources (UPS)
- Power Factor Correction (PFC)

BIDD05N60T Insulated Gate Bipolar Transistor (IGBT)

General Information

The Bourns® Model BIDD05N60T IGBT device combines technology from a MOS gate and a bipolar transistor for an optimum component for high voltage and high current applications. This device uses Trench-Gate Field-Stop technology providing greater control of dynamic characteristics with a lower Collector-Emitter Saturation Voltage ($V_{CE(sat)}$) and fewer switching losses. In addition, this structure improves the robustness of the device.

Additional Information

Click these links for more information:



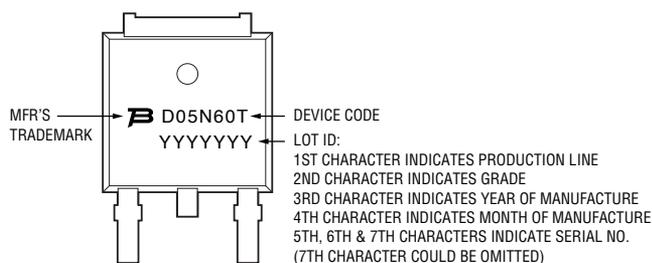
Maximum Electrical Ratings ($T_C = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CES}	600	V
Continuous Collector Current ($T_C = 25\text{ }^\circ\text{C}$), limited by T_{jmax}	I_C	10	A
Continuous Collector Current ($T_C = 100\text{ }^\circ\text{C}$), limited by T_{jmax}	I_C	5	A
Pulsed Collector Current, t_p limited by T_{jmax}	I_{CP}	15	A
Gate-Emitter Voltage	V_{GE}	± 30	V
Continuous Forward Current ($T_C = 25\text{ }^\circ\text{C}$), limited by T_{jmax}	I_F	10	A
Short-circuit Withstand Time ($V_{CE} = 300\text{ V}$, $V_{GE} = 15\text{ V}$)	T_{SC}	10	μs
Total Power Dissipation	P_{total}	82	W
Storage Temperature	T_{STG}	-55 to +150	$^\circ\text{C}$
Operating Junction Temperature	T_j	-55 to +150	$^\circ\text{C}$

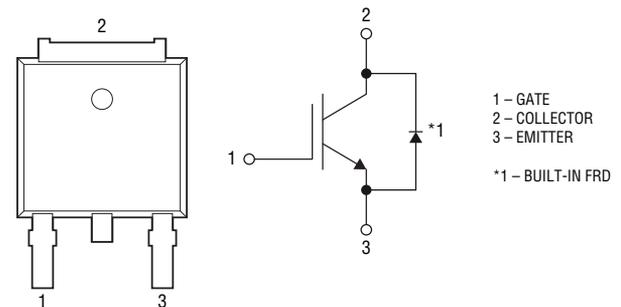
Thermal Resistance

Parameter	Symbol	Max	Unit
IGBT Thermal Resistance Junction - Case	$R_{th(j-c)}_{IGBT}$	1.51	$^\circ\text{C/W}$
Diode Thermal Resistance Junction - Case	$R_{th(j-c)}_{Diode}$	2.14	$^\circ\text{C/W}$

Typical Part Marking



Internal Circuit



WARNING Cancer and Reproductive Harm
www.P65Warnings.ca.gov

*RoHS Directive 2015/863, Mar 31, 2015 and Annex. Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

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BIDD05N60T Insulated Gate Bipolar Transistor (IGBT)

BOURNS®

Static Electrical Characteristics ($T_C = 25\text{ }^\circ\text{C}$, Unless Otherwise Specified)

Parameter	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Collector-Emitter Breakdown Voltage	BV_{CES}	$V_{GE} = 0\text{ V}, I_C = 250\text{ }\mu\text{A}$	600	—	—	V
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	$V_{GE} = 15\text{ V}, I_C = 5\text{ A}$ $T_C = 25\text{ }^\circ\text{C}$	—	1.5	2.0	V
		$V_{GE} = 15\text{ V}, I_C = 5\text{ A}$ $T_C = 125\text{ }^\circ\text{C}$	—	1.7	—	
Diode Forward On-Voltage	V_F	$I_F = 5\text{ A}, T_C = 25\text{ }^\circ\text{C}$	—	1.3	1.8	V
		$I_F = 5\text{ A}, T_C = 125\text{ }^\circ\text{C}$	—	1.1	—	V
Gate Threshold Voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}, I_C = 250\text{ }\mu\text{A}$	3.5	5.5	6.5	V
Collector Cut-off Current	I_{CES}	$V_{GE} = 0\text{ V}, V_{CE} = 600\text{ V}$	—	—	200	μA
Gate-Emitter Leakage Current	I_{GES}	$V_{CE} = 0\text{ V}, V_{GE} = \pm 20\text{ V}$	—	—	± 400	nA

Dynamic Electrical Characteristics ($T_C = 25\text{ }^\circ\text{C}$, Unless Otherwise Specified)

Parameter	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Input Capacitance	C_{ies}	$V_{CE} = 30\text{ V}, V_{GE} = 0\text{ V},$ $f = 1\text{ MHz}$	—	340	—	pF
Output Capacitance	C_{oes}		—	26	—	
Reverse Transfer Capacitance	C_{res}		—	7.6	—	
Total Gate Charge	Q_g	$V_{CE} = 400\text{ V}, V_{GE} = 15\text{ V}$ $I_C = 5.0\text{ A}$	—	18.5	—	nC
Gate-Emitter Charge	Q_{ge}		—	5.1	—	
Gate-Collector Charge	Q_{gc}		—	8.6	—	

IGBT Switching Characteristics (Inductive Load, $T_C = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter ($T_C = 25\text{ }^\circ\text{C}$)	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Turn-on Delay Time	$t_{d(on)}$	$V_{CE} = 400\text{ V}, V_{GE} = 15\text{ V}$ $I_C = 5.0\text{ A}, R_G = 10\text{ }\Omega$	—	7	—	ns
Current Rise Time	t_r		—	14	—	ns
Turn-off Delay Time	$t_{d(off)}$		—	18	—	ns
Current Fall Time	t_f		—	145	—	ns
Turn-on Switching Energy	E_{on}		—	0.2	—	mJ
Turn-off Switching Energy	E_{off}		—	0.07	—	mJ
Total Switching Energy	E_{ts}		—	0.27	—	mJ

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BIDD05N60T Insulated Gate Bipolar Transistor (IGBT)

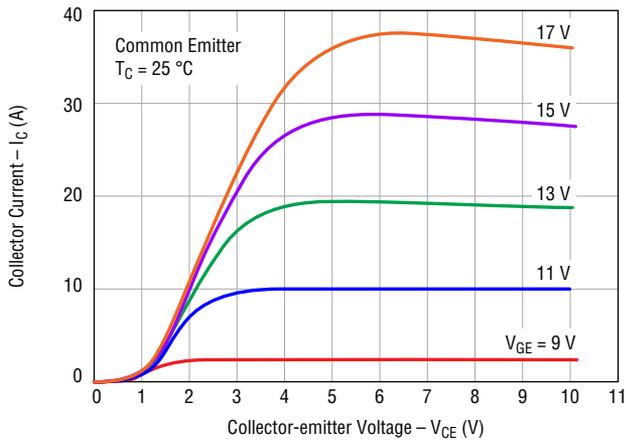
BOURNS®

Diode Switching Characteristics ($T_C = 25\text{ }^\circ\text{C}$, unless otherwise specified)

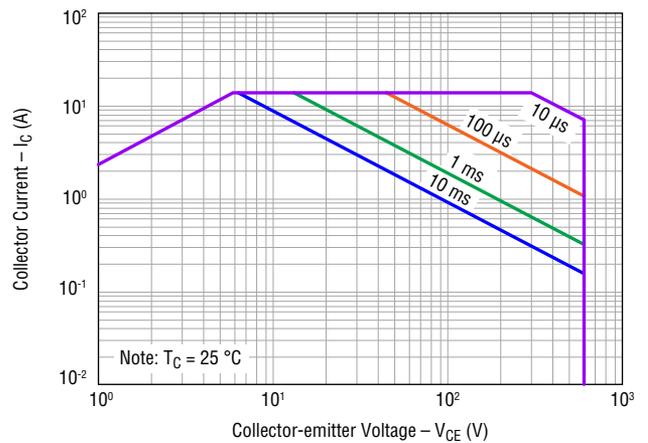
Parameter ($T_C = 25\text{ }^\circ\text{C}$)	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Reverse Recovery Time	t_{rr}	$di_F/dt = 200\text{ A}/\mu\text{s}$ $I_F = 5.0\text{ A}$	—	40	—	ns
Reverse Recovery Charge	Q_{rr}		—	80	—	nC

Electrical Characteristic Performance

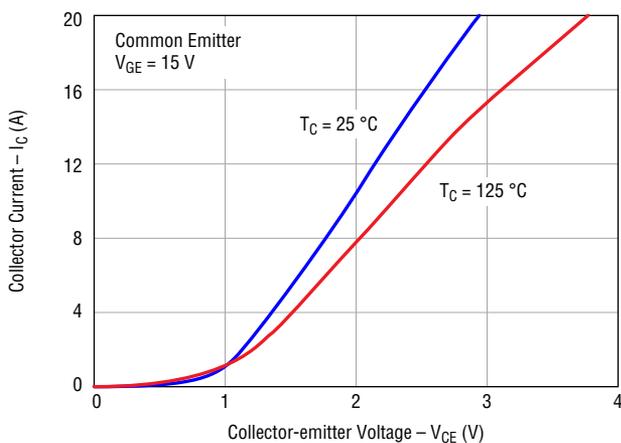
Typical Output Characteristics



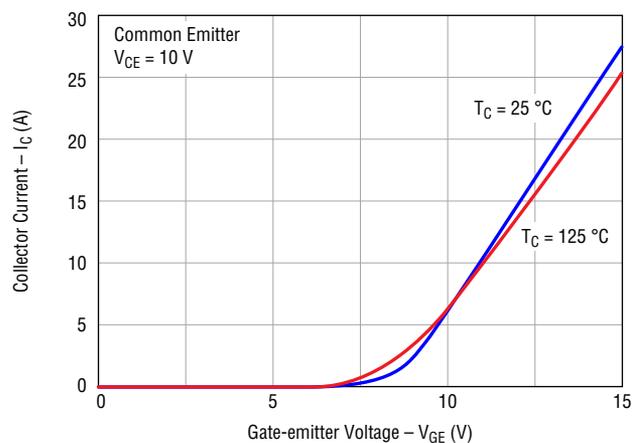
Forward Bias Safe Operating Area



Typical Saturation Voltage Characteristics



Typical Transfer Characteristics



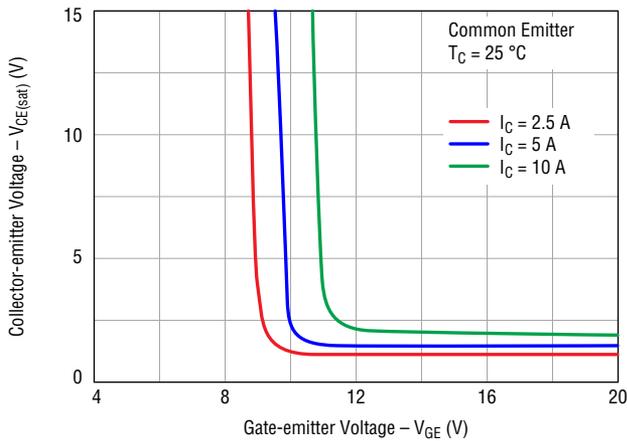
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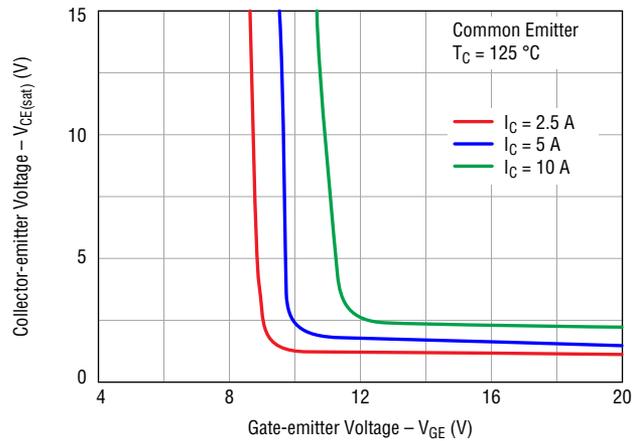
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Electrical Characteristic Performance (continued)

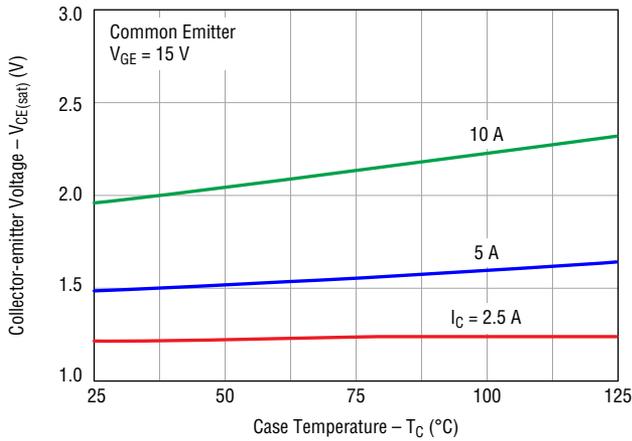
Typical $V_{CE(sat)}$ vs V_{GE} @ $T_C = 25^\circ\text{C}$



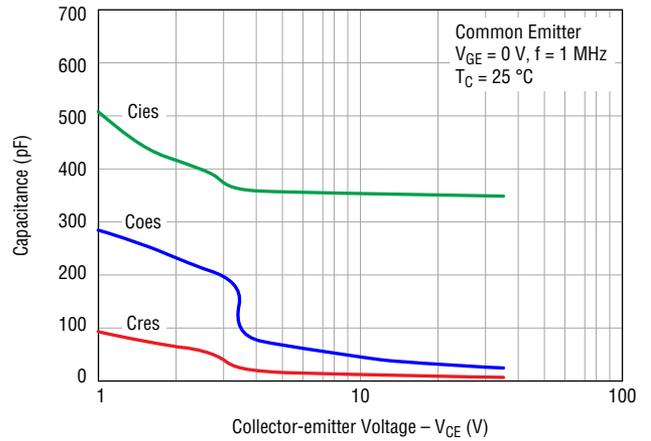
Typical $V_{CE(sat)}$ vs V_{GE} @ $T_C = 125^\circ\text{C}$



Typical $V_{CE(sat)}$ vs Case Temperature



Typical Capacitance Characteristics



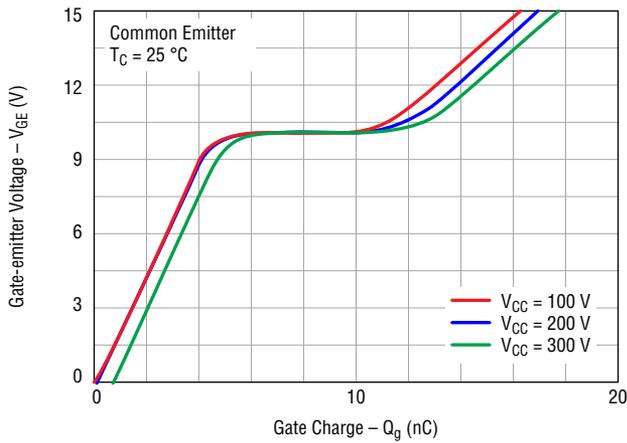
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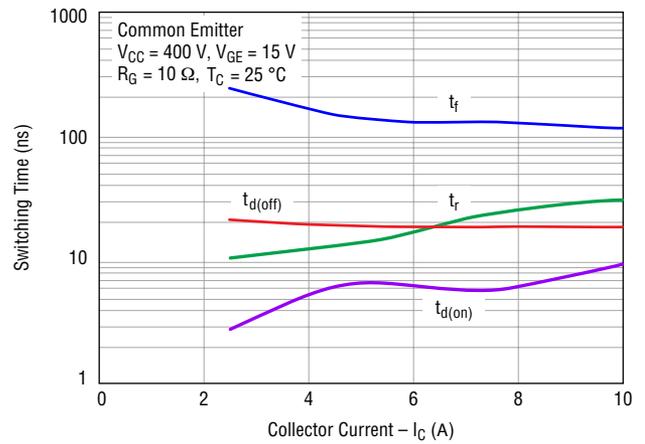
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Electrical Characteristic Performance (continued)

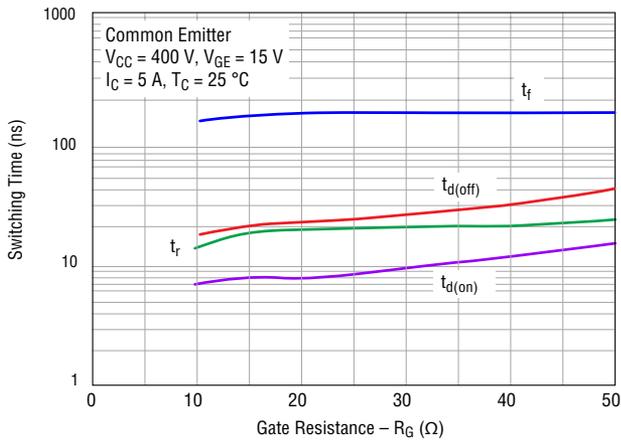
Typical Gate Charge Characteristic



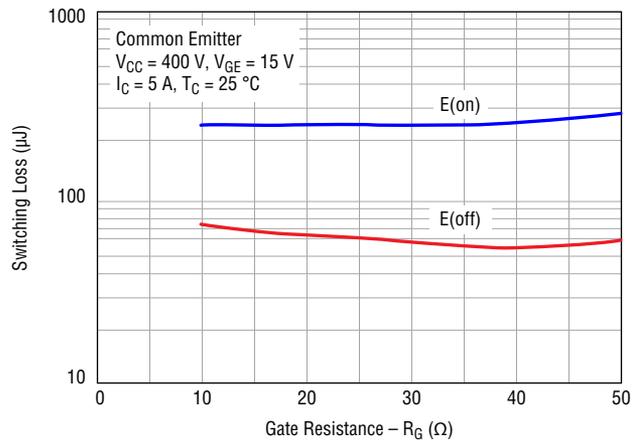
Typical Switching Time Characteristics vs I_C



Typical Switching Time Characteristics vs R_G

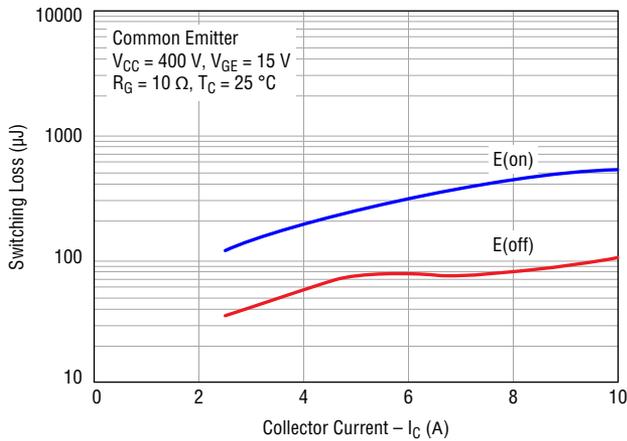


Typical Switching Loss vs R_G

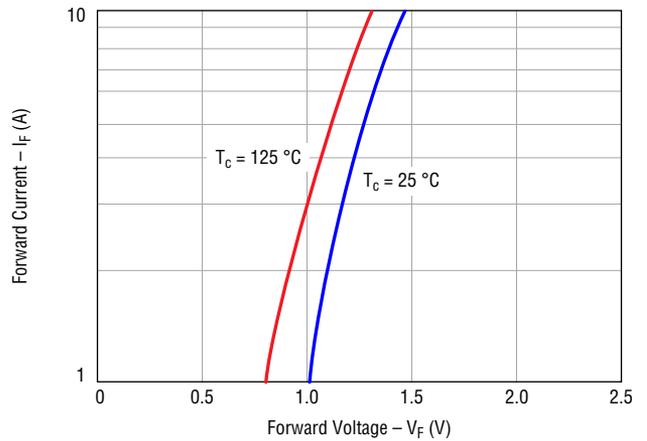


Electrical Characteristic Performance (continued)

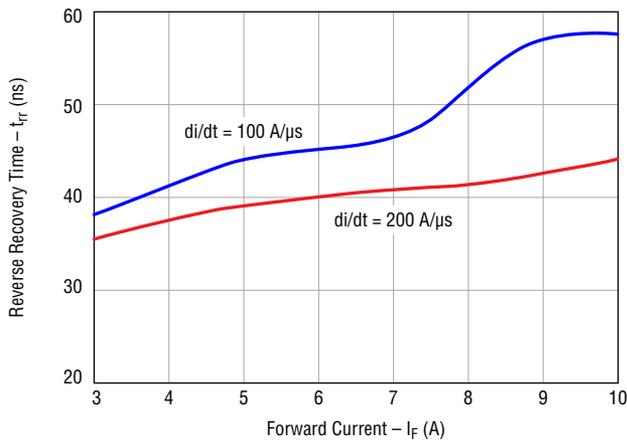
Typical Switching Loss Characteristics vs I_C



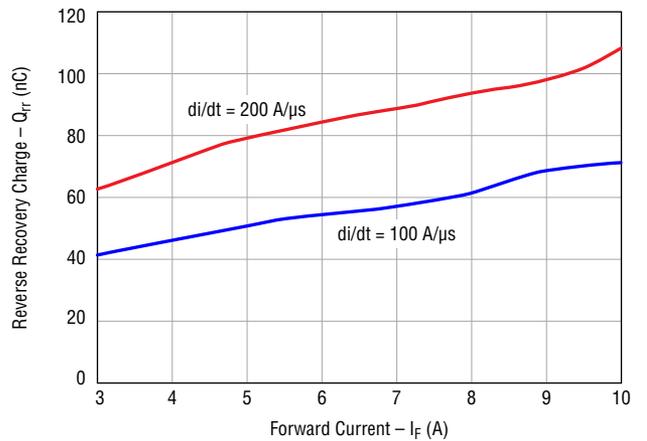
Typical Diode I_F vs V_F



Typical Reverse Recovery Time vs I_F



Typical Reverse Recovery Charge vs I_F



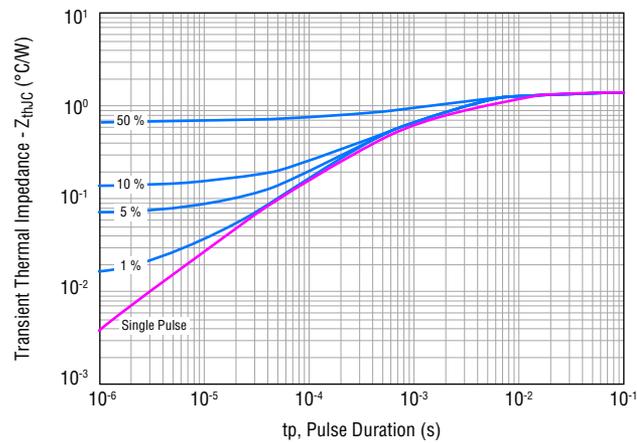
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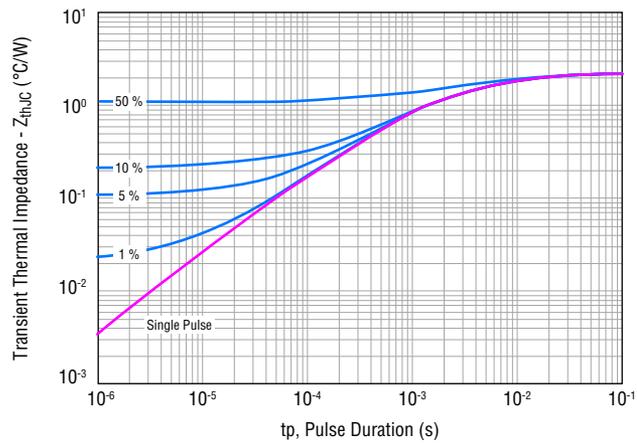
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Electrical Characteristic Performance (continued)

IGBT Transient Thermal Impedance vs $t_{p(on)}$ Duration ($D=t_p/T$)



Diode Transient Thermal Impedance vs $t_{p(on)}$ Duration ($D=t_p/T$)



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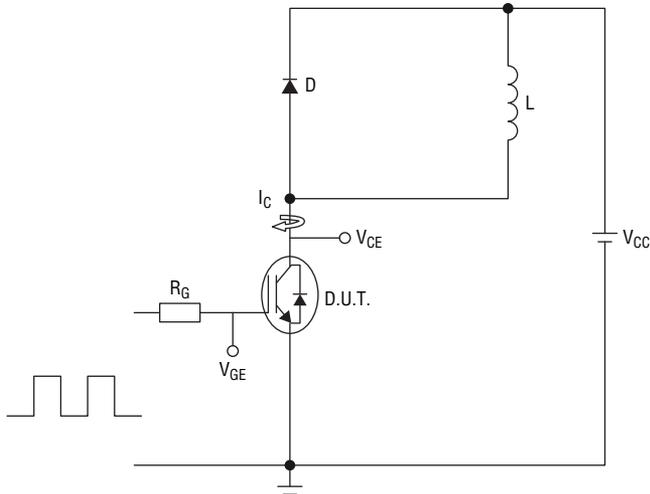
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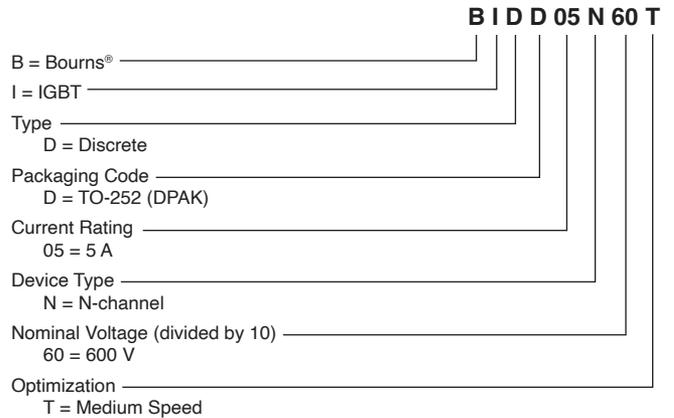
BOURNS®

Inductive Load Test Circuit



$L = 11.2 \text{ mH}$, $V_{CE} = 400 \text{ V}$, $V_{GE} = 15 \text{ V}$, $I_C = 5 \text{ A}$, $R_G = 10 \Omega$

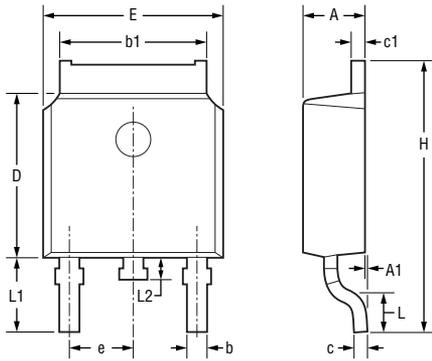
How to Order



Environmental Characteristics

Moisture Sensitivity Level3
 ESD Class (HBM) 1B

Product Dimensions



DIMENSIONS: $\frac{\text{MM}}{\text{(INCHES)}}$

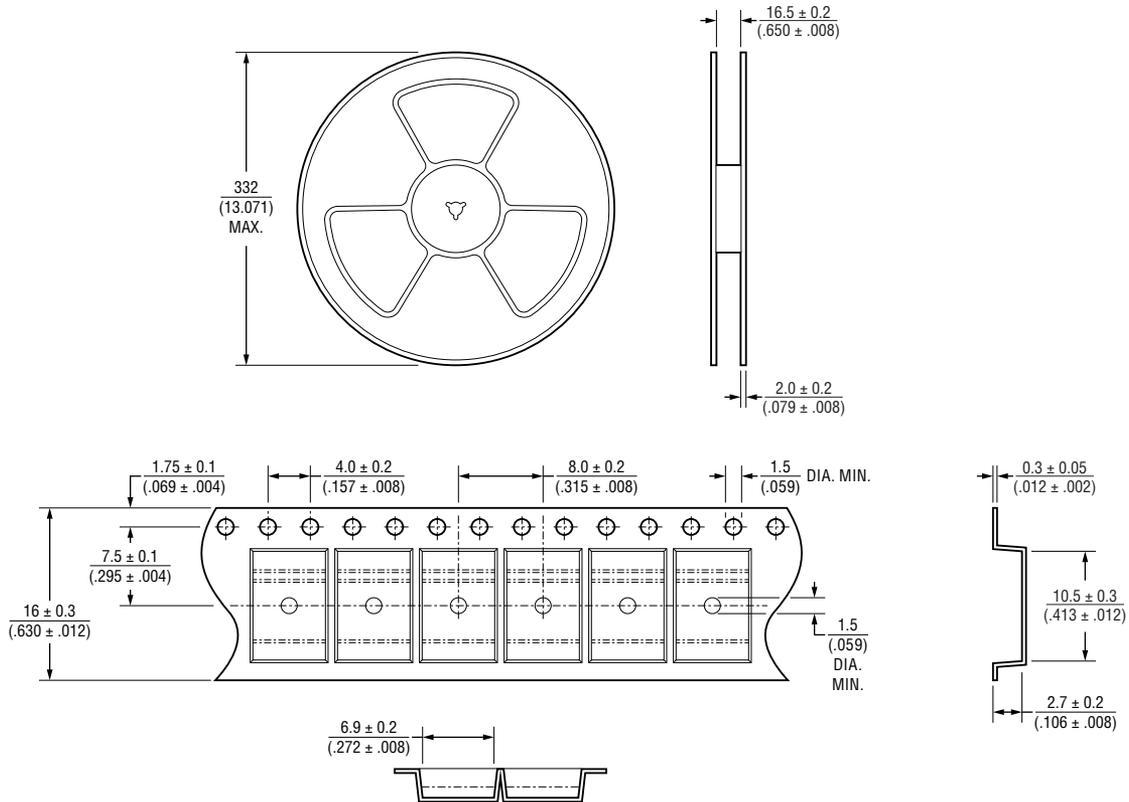
Symbol	Min.	Nom.	Max.
A	$\frac{2.10}{(.083)}$	$\frac{2.30}{(.091)}$	$\frac{2.50}{(.098)}$
A1	0	—	$\frac{0.127}{(.005)}$
b	$\frac{0.66}{(.026)}$	$\frac{0.76}{(.030)}$	$\frac{0.89}{(.035)}$
b1	$\frac{5.10}{(.201)}$	$\frac{5.33}{(.210)}$	$\frac{5.46}{(.215)}$
c	$\frac{0.45}{(.018)}$	—	$\frac{0.65}{(.026)}$
c1	$\frac{0.45}{(.018)}$	—	$\frac{0.65}{(.026)}$
D	$\frac{5.80}{(.228)}$	$\frac{6.10}{(.240)}$	$\frac{6.40}{(.252)}$
E	$\frac{6.30}{(.248)}$	$\frac{6.60}{(.260)}$	$\frac{6.90}{(.272)}$
e	$\frac{2.30}{(.091)}$ TYP		
H	$\frac{9.60}{(.378)}$	$\frac{10.10}{(.398)}$	$\frac{10.60}{(.417)}$
L	$\frac{1.40}{(.055)}$	$\frac{1.50}{(.059)}$	$\frac{1.70}{(.067)}$
L1	$\frac{2.90}{(.114)}$ REF		
L2	$\frac{0.60}{(.024)}$	$\frac{0.80}{(.031)}$	$\frac{1.00}{(.039)}$

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BIDD05N60T Insulated Gate Bipolar Transistor (IGBT)

BOURNS®

Packaging Specifications



DIMENSIONS: $\frac{\text{MM}}{\text{(INCHES)}}$ USER DIRECTION OF FEED
QTY: 2500 PCS PER REEL

BOURNS®

Asia-Pacific: Tel: +886-2 2562-4117 • Email: asiacus@bourns.com

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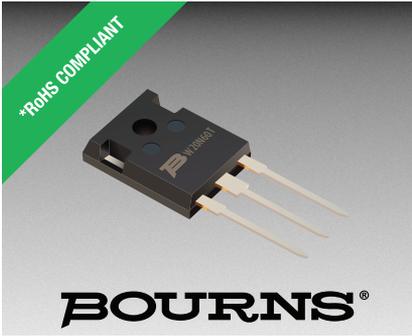
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Web Page: <http://www.bourns.com/legal/disclaimers-terms-and-policies>

PDF: <http://www.bourns.com/docs/Legal/disclaimer.pdf>



Features

- 600 V, 20 A, Low Collector-Emitter Saturation Voltage ($V_{CE(sat)}$)
- Trench-Gate Field-Stop technology
- Optimized for conduction
- Low switching loss
- RoHS compliant*

Applications

- Switch-Mode Power Supplies (SMPS)
- Uninterruptible Power Sources (UPS)
- Power Factor Correction (PFC)
- Stepper motors

BOURNS®

BIDW20N60T Insulated Gate Bipolar Transistor (IGBT)

General Information

The Bourns® Model BIDW20N60T IGBT device combines technology from a MOS gate and a bipolar transistor for an optimum component for high voltage and high current applications. This device uses Trench-Gate Field-Stop technology providing greater control of dynamic characteristics with a lower conduction loss and fewer switching losses. In addition, this structure provides a positive temperature coefficient.

Additional Information

Click these links for more information:



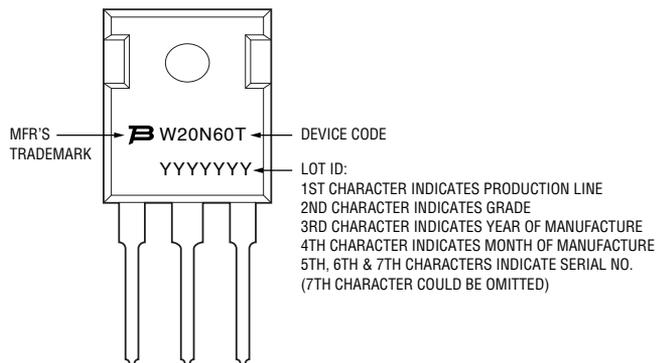
Maximum Electrical Ratings ($T_C = 25\text{ °C}$, unless otherwise specified)

Parameter	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CES}	600	V
Continuous Collector Current ($T_C = 25\text{ °C}$), limited by T_{jmax}	I_C	40	A
Continuous Collector Current ($T_C = 100\text{ °C}$), limited by T_{jmax}	I_C	20	A
Pulsed Collector Current, t_p limited by T_{jmax}	I_{CP}	60	A
Gate-Emitter Voltage	V_{GE}	± 20	V
Continuous Forward Current ($T_C = 25\text{ °C}$), limited by T_{jmax}	I_F	40	A
Continuous Forward Current ($T_C = 100\text{ °C}$), limited by T_{jmax}	I_F	20	A
Short-circuit Withstand Time ($V_{CE} = 300\text{ V}$, $V_{GE} = 15\text{ V}$)	T_{SC}	10	μs
Total Power Dissipation	P_{total}	192	W
Storage Temperature	T_{STG}	-55 to +150	$^{\circ}C$
Operating Junction Temperature	T_j	-55 to +150	$^{\circ}C$

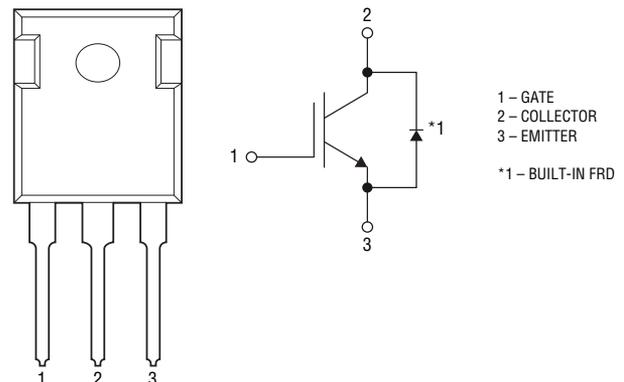
Thermal Resistance

Parameter	Symbol	Max	Unit
IGBT Thermal Resistance Junction - Case	$R_{th(j-c)}_{IGBT}$	0.65	$^{\circ}C/W$
Diode Thermal Resistance Junction - Case	$R_{th(j-c)}_{Diode}$	1.19	$^{\circ}C/W$

Typical Part Marking



Internal Circuit



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BIDW20N60T Insulated Gate Bipolar Transistor (IGBT)

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Static Electrical Characteristics ($T_C = 25\text{ }^\circ\text{C}$, Unless Otherwise Specified)

Parameter	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Collector-Emitter Breakdown Voltage	BV_{CES}	$V_{GE} = 0\text{ V}, I_C = 250\text{ }\mu\text{A}$	600	—	—	V
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	$V_{GE} = 15\text{ V}, I_C = 20\text{ A}$ $T_C = 25\text{ }^\circ\text{C}$	—	1.7	2.4	V
		$V_{GE} = 15\text{ V}, I_C = 20\text{ A}$ $T_C = 125\text{ }^\circ\text{C}$	—	1.9	—	
Diode Forward On-Voltage	V_F	$I_F = 20\text{ A}, T_C = 25\text{ }^\circ\text{C}$	—	1.8	—	V
		$I_F = 20\text{ A}, T_C = 125\text{ }^\circ\text{C}$	—	1.5	—	V
Gate Threshold Voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}, I_C = 250\text{ }\mu\text{A}$	4.0	5.0	6.5	V
Collector Cut-off Current	I_{CES}	$V_{GE} = 0\text{ V}, V_{CE} = 600\text{ V}$	—	—	200	μA
Gate-Emitter Leakage Current	I_{GES}	$V_{CE} = 0\text{ V}, V_{GE} = \pm 20\text{ V}$	—	—	± 400	nA

Dynamic Electrical Characteristics ($T_C = 25\text{ }^\circ\text{C}$, Unless Otherwise Specified)

Parameter	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Input Capacitance	C_{ies}	$V_{CE} = 30\text{ V}, V_{GE} = 0\text{ V},$ $f = 1\text{ MHz}$	—	1100	—	pF
Output Capacitance	C_{oes}		—	55	—	
Reverse Transfer Capacitance	C_{res}		—	22	—	
Total Gate Charge	Q_g	$V_{CE} = 400\text{ V}, V_{GE} = 15\text{ V}$ $I_C = 20.0\text{ A}$	—	52	—	nC
Gate-Emitter Charge	Q_{ge}		—	15	—	
Gate-Collector Charge	Q_{gc}		—	22	—	

IGBT Switching Characteristics (Inductive Load, $T_C = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Turn-on Delay Time	$t_{d(on)}$	$V_{CE} = 400\text{ V}, V_{GE} = 15\text{ V}$ $I_C = 20.0\text{ A}, R_G = 10\text{ }\Omega$	—	19	—	ns
Current Rise Time	t_r		—	55	—	ns
Turn-off Delay Time	$t_{d(off)}$		—	48	—	ns
Current Fall Time	t_f		—	115	—	ns
Turn-on Switching Energy	E_{on}		—	1	—	mJ
Turn-off Switching Energy	E_{off}		—	0.3	—	mJ
Total Switching Energy	E_{ts}		—	1.3	—	mJ

Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

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BIDW20N60T Insulated Gate Bipolar Transistor (IGBT)

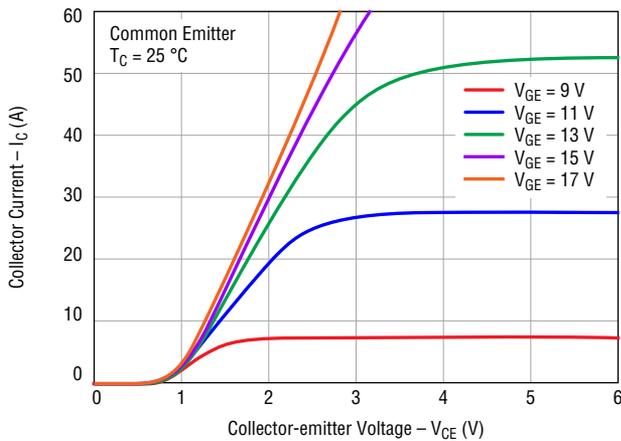
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Diode Switching Characteristics ($T_C = 25\text{ }^\circ\text{C}$, unless otherwise specified)

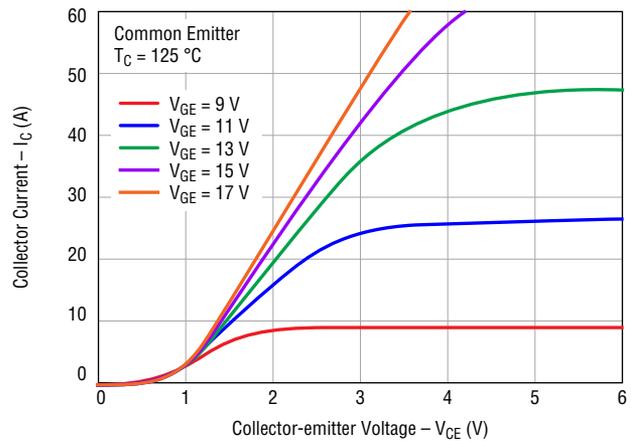
Parameter	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Reverse Recovery Time	t_{rr}	$di_F/dt = 200\text{ A}/\mu\text{s}$ $I_F = 20.0\text{ A}$	—	33.7	—	ns
Reverse Recovery Charge	Q_{rr}		—	73.3	—	nC

Electrical Characteristic Performance

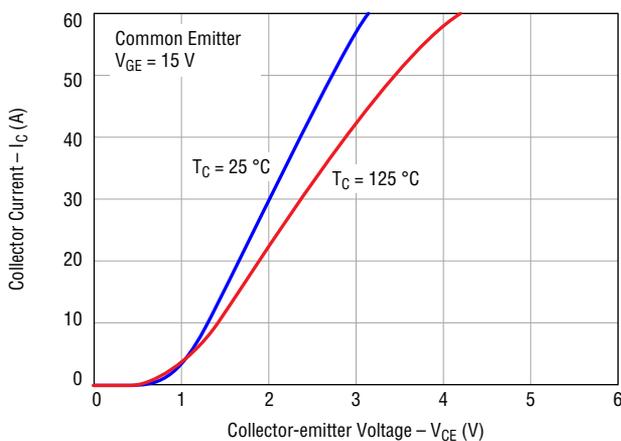
Typical Output Characteristics



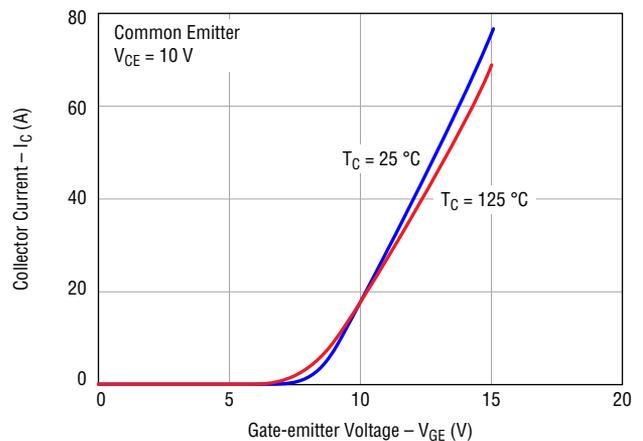
Typical Output Characteristics



Typical Saturation Voltage Characteristics



Typical Transfer Characteristics



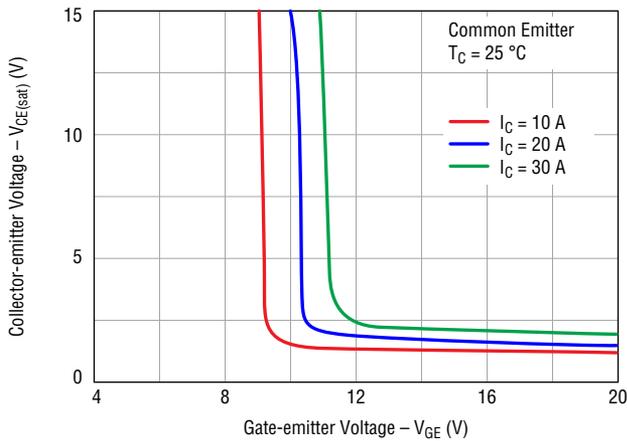
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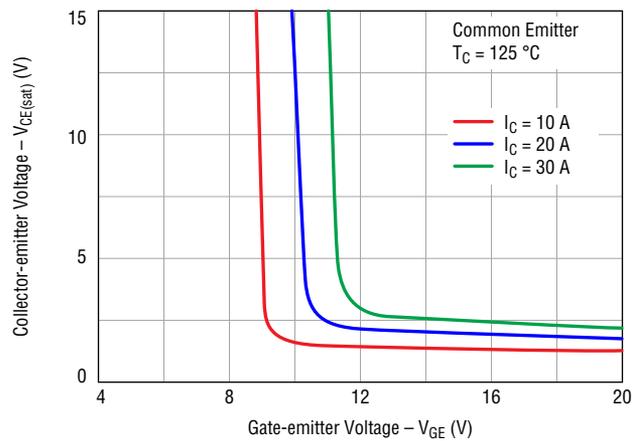
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Electrical Characteristic Performance (continued)

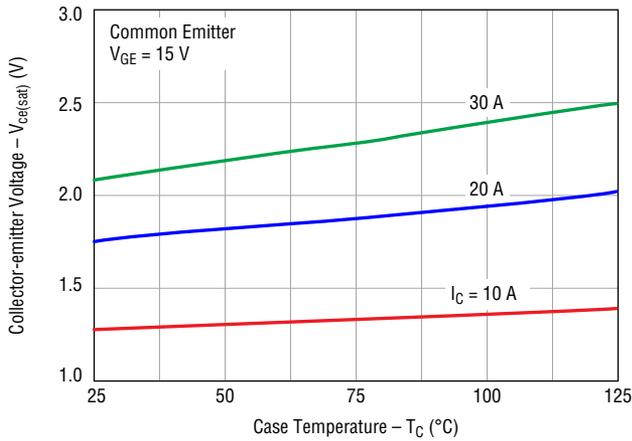
Typical $V_{CE(sat)}$ vs V_{GE} @ $T_C = 25^\circ\text{C}$



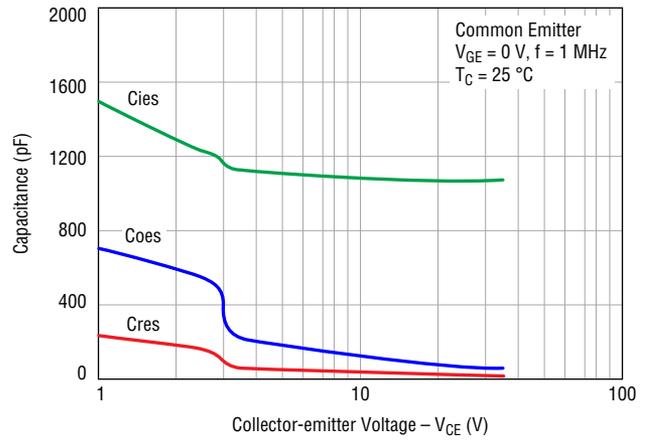
Typical $V_{CE(sat)}$ vs V_{GE} @ $T_C = 125^\circ\text{C}$



Typical $V_{CE(sat)}$ vs Case Temperature



Typical Capacitance Characteristics



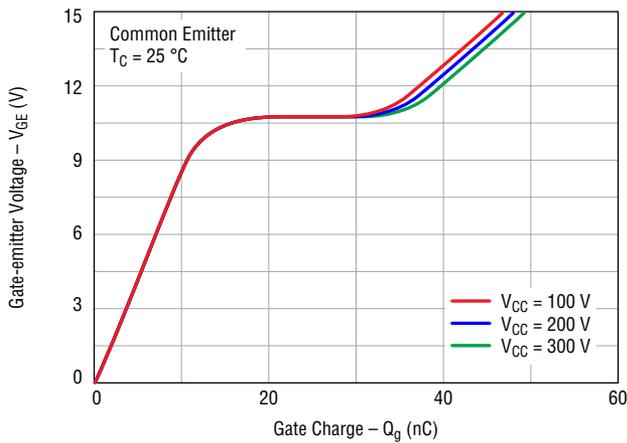
Specifications are subject to change without notice.

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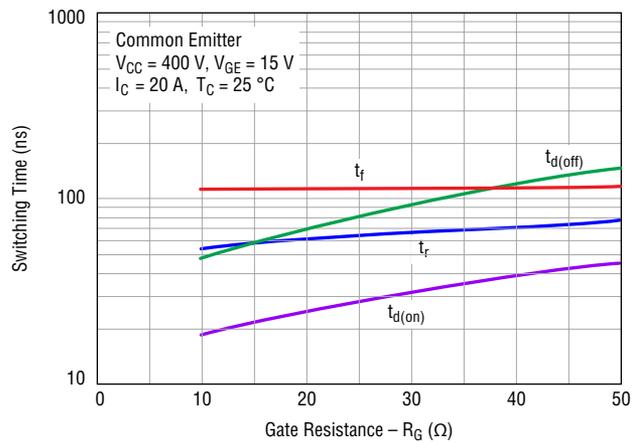
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Electrical Characteristic Performance (continued)

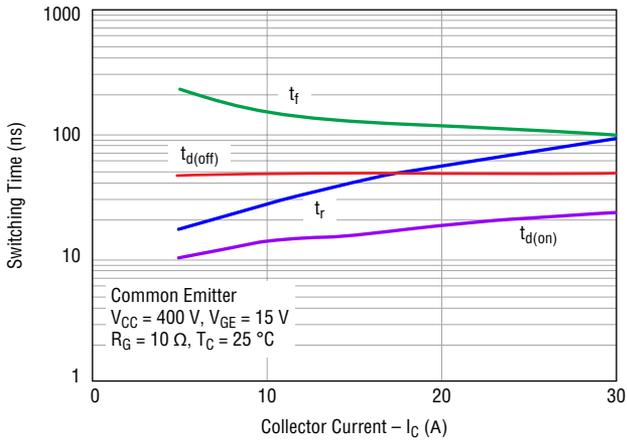
Typical Gate Charge Characteristics



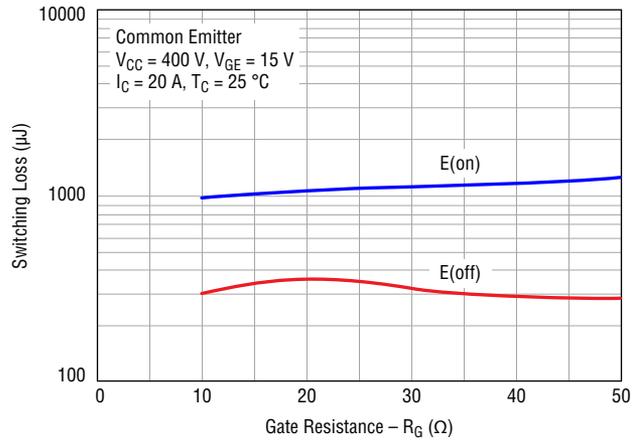
Typical Switching Time Characteristics vs R_G



Typical Switching Time Characteristics vs I_C



Typical Switching Loss vs R_G

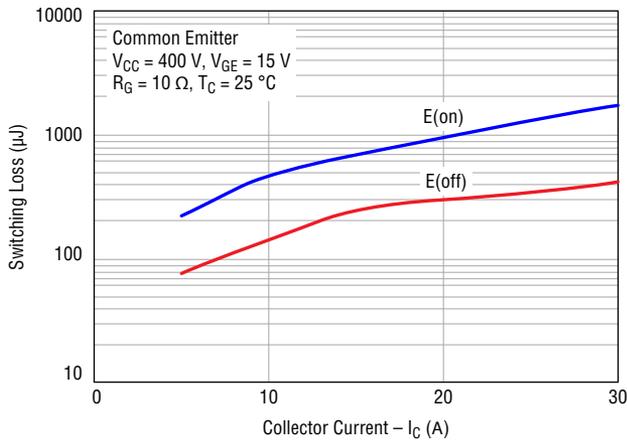


BIDW20N60T Insulated Gate Bipolar Transistor (IGBT)

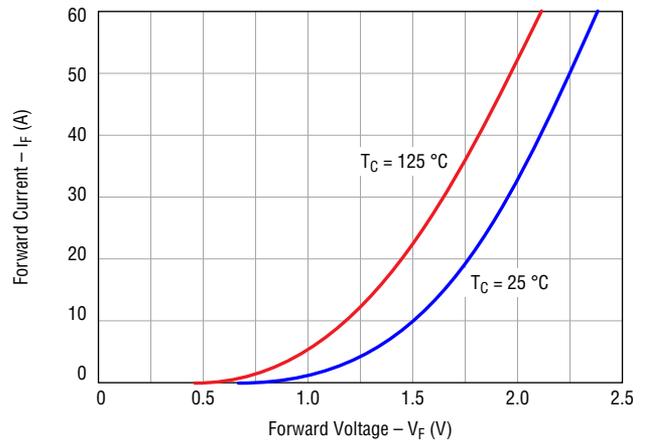
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Electrical Characteristic Performance (continued)

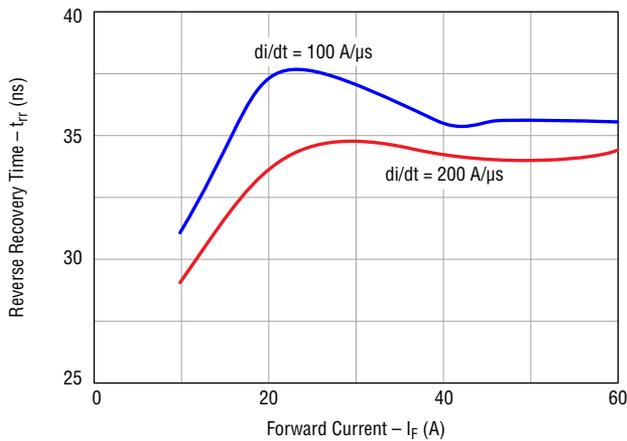
Typical Switching Loss Characteristics vs I_C



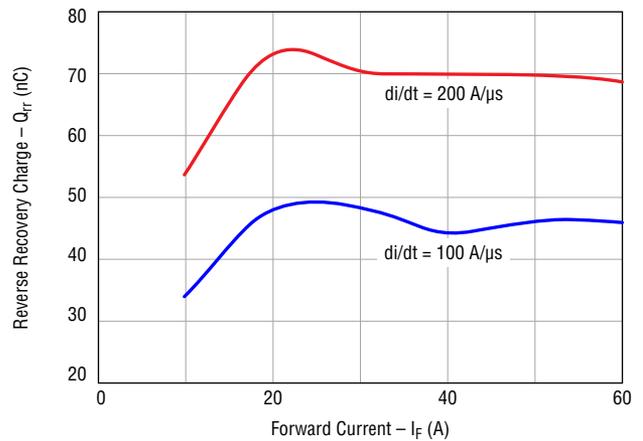
Typical Diode I_F vs V_F



Typical Reverse Recovery Time vs I_F



Typical Reverse Recovery Charge vs I_F



Specifications are subject to change without notice.

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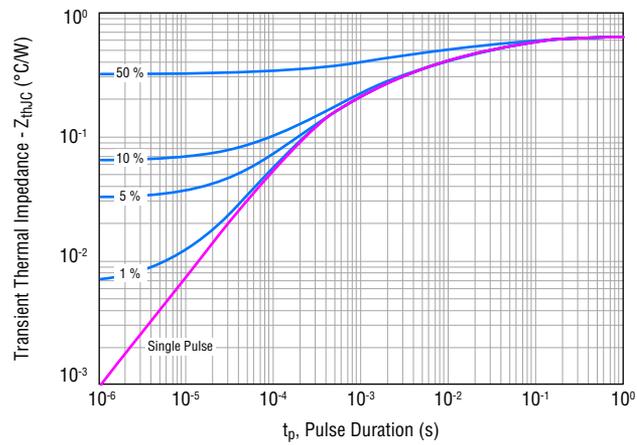
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BIDW20N60T Insulated Gate Bipolar Transistor (IGBT)

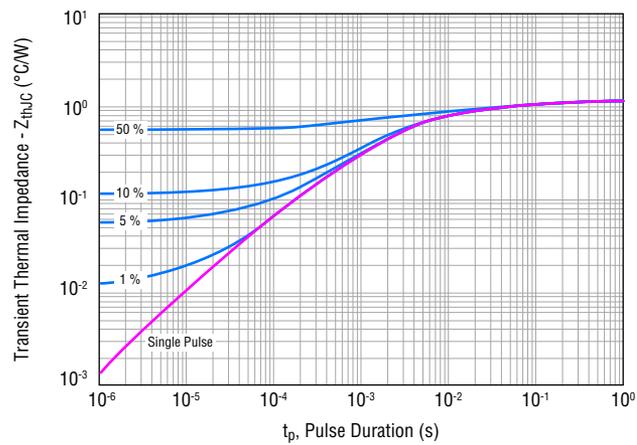
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Electrical Characteristic Performance (continued)

IGBT Transient Thermal Impedance vs $t_{p(on)}$ Duration ($D=t_p/T$)



Diode Transient Thermal Impedance vs $t_{p(on)}$ Duration ($D=t_p/T$)



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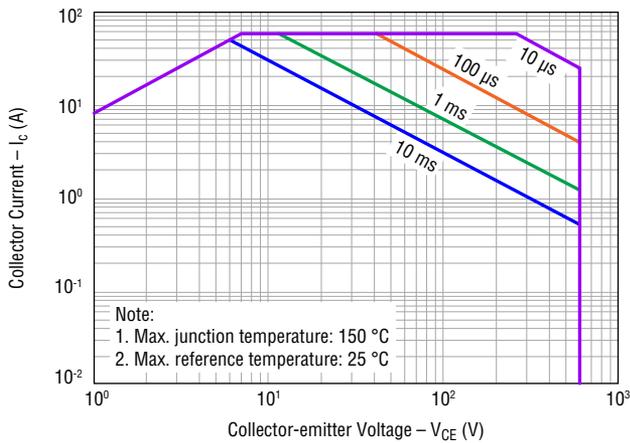
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BIDW20N60T Insulated Gate Bipolar Transistor (IGBT)



Electrical Characteristic Performance (continued)

Forward Bias Safe Operating Area

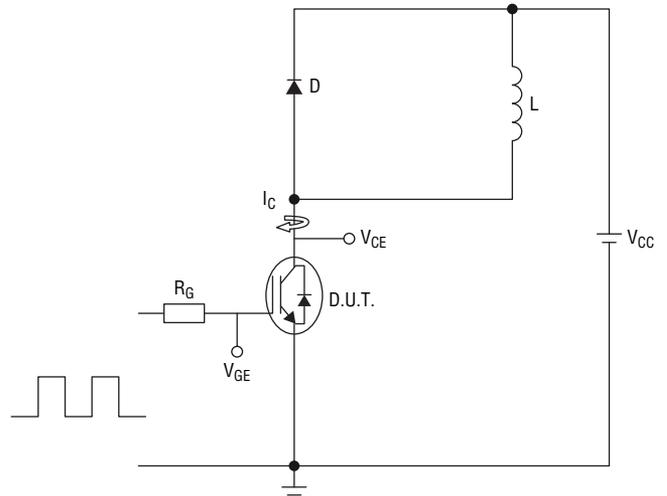


How to Order

B I D W 20 N 60 T

- B = Bourns®
- I = IGBT
- Type
 - D = Discrete
- Package Code
 - W = TO-247
- Current Rating
 - 20 = 20 A
- Device Type
 - N = N-channel
- Nominal Voltage (divided by 10)
 - 60 = 600 V
- Optimization
 - T = Medium Speed

Inductive Load Test Circuit



$L = 2.8 \text{ mH}$, $V_{CE} = 400 \text{ V}$, $V_{GE} = 15 \text{ V}$, $I_C = 20 \text{ A}$, $R_G = 10 \Omega$

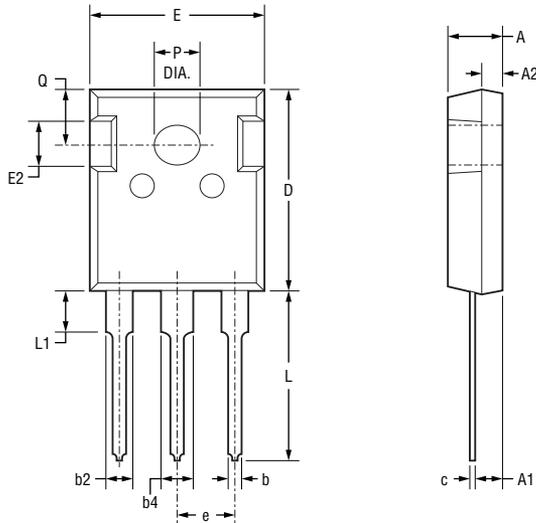
Environmental Characteristics

ESD Class (HBM) 1C

BIDW20N60T Insulated Gate Bipolar Transistor (IGBT)

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Product Dimensions



DIMENSIONS: $\frac{\text{MM}}{\text{(INCHES)}}$

Symbol	Min.	Nom.	Max.
A	$\frac{4.80}{(.189)}$	$\frac{5.00}{(.197)}$	$\frac{5.20}{(.205)}$
A1	$\frac{2.21}{(.087)}$	$\frac{2.41}{(.095)}$	$\frac{2.59}{(.102)}$
A2	$\frac{1.85}{(.073)}$	$\frac{2.00}{(.079)}$	$\frac{2.15}{(.085)}$
b	$\frac{1.11}{(.044)}$	—	$\frac{1.36}{(.054)}$
b2	$\frac{1.91}{(.075)}$	—	$\frac{2.25}{(.089)}$
b4	$\frac{2.91}{(.115)}$	—	$\frac{3.25}{(.128)}$
c	$\frac{0.51}{(.020)}$	—	$\frac{0.75}{(.030)}$
D	$\frac{20.80}{(.819)}$	$\frac{21.00}{(.827)}$	$\frac{21.30}{(.839)}$
E	$\frac{15.50}{(.610)}$	$\frac{15.80}{(.622)}$	$\frac{16.10}{(.634)}$
E2	$\frac{4.40}{(.173)}$	$\frac{5.00}{(.197)}$	$\frac{5.20}{(.205)}$
e	$\frac{5.44}{(.214)} \text{ BSC}$		
L	$\frac{19.72}{(.776)}$	$\frac{19.92}{(.784)}$	$\frac{20.22}{(.796)}$
L1	—	—	$\frac{4.30}{(.169)}$
P	$\frac{3.40}{(.134)}$	—	$\frac{3.80}{(.150)}$
Q	$\frac{5.60}{(.220)}$	$\frac{5.80}{(.228)}$	$\frac{6.00}{(.236)}$

Packaging Specifications

BIDW20N60T 30 pieces per tube

BOURNS®

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EMEA: Tel: +36 88 885 877

Email: eurocus@bourns.com

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Email: americus@bourns.com

www.bourns.com

07/22

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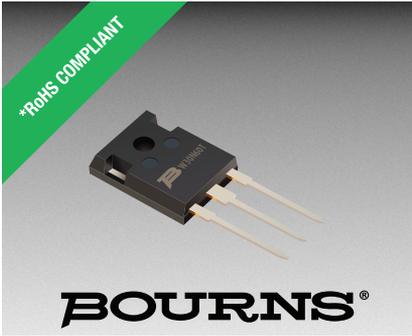
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Web Page: <http://www.bourns.com/legal/disclaimers-terms-and-policies>

PDF: <http://www.bourns.com/docs/Legal/disclaimer.pdf>



Features

- 600 V, 30 A, Low Collector-Emitter Saturation Voltage ($V_{CE(sat)}$)
- Trench-Gate Field-Stop technology
- Optimized for conduction
- RoHS compliant*

Applications

- Switch-Mode Power Supplies (SMPS)
- Uninterruptible Power Sources (UPS)
- Power Factor Correction (PFC)
- Induction heating

BOURNS®

BIDW30N60T Insulated Gate Bipolar Transistor (IGBT)

General Information

The Bourns® Model BIDW30N60T IGBT device combines technology from a MOS gate and a bipolar transistor for an optimum component for high voltage and high current applications. This device uses Trench-Gate Field-Stop technology providing greater control of dynamic characteristics with a lower Collector-Emitter Saturation Voltage ($V_{CE(sat)}$) and fewer switching losses. In addition, this structure gives a lower thermal resistance $R_{(th)}$.

Additional Information

Click these links for more information:



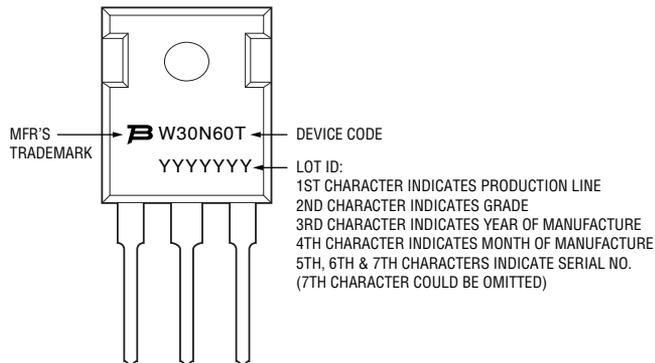
Maximum Electrical Ratings ($T_C = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CES}	600	V
Continuous Collector Current ($T_C = 25\text{ }^\circ\text{C}$), limited by T_{jmax}	I_C	60	A
Continuous Collector Current ($T_C = 100\text{ }^\circ\text{C}$), limited by T_{jmax}	I_C	30	A
Pulsed Collector Current, t_p limited by T_{jmax}	I_{CP}	90	A
Gate-Emitter Voltage	V_{GE}	± 20	V
Continuous Forward Current ($T_C = 25\text{ }^\circ\text{C}$), limited by T_{jmax}	I_F	60	A
Continuous Forward Current ($T_C = 100\text{ }^\circ\text{C}$), limited by T_{jmax}	I_F	30	A
Short-circuit Withstand Time ($V_{CE} = 300\text{ V}$, $V_{GE} = 15\text{ V}$)	T_{SC}	10	μs
Total Power Dissipation	P_{total}	230	W
Storage Temperature	T_{STG}	-55 to +150	$^\circ\text{C}$
Operating Junction Temperature	T_j	-55 to +150	$^\circ\text{C}$

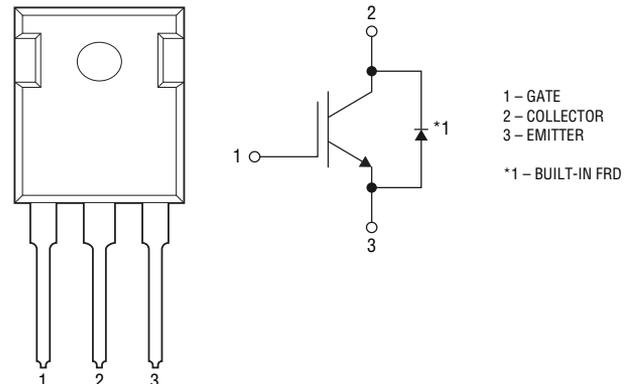
Thermal Resistance

Parameter	Symbol	Max	Unit
IGBT Thermal Resistance Junction - Case	$R_{th(j-c)}_{IGBT}$	0.54	$^\circ\text{C/W}$
Diode Thermal Resistance Junction - Case	$R_{th(j-c)}_{Diode}$	1.2	$^\circ\text{C/W}$

Typical Part Marking



Internal Circuit



*RoHS Directive 2015/863, Mar 31, 2015 and Annex. Specifications are subject to change without notice. Users should verify actual device performance in their specific applications. The products described herein and this document are subject to specific legal disclaimers as set forth on the last page of this document, and at www.bourns.com/docs/legal/disclaimer.pdf.

BIDW30N60T Insulated Gate Bipolar Transistor (IGBT)

BOURNS®

Static Electrical Characteristics ($T_C = 25\text{ }^\circ\text{C}$, Unless Otherwise Specified)

Parameter	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Collector-Emitter Breakdown Voltage	BV_{CES}	$V_{GE} = 0\text{ V}, I_C = 250\text{ }\mu\text{A}$	600	—	—	V
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	$V_{GE} = 15\text{ V}, I_C = 30\text{ A}$ $T_C = 25\text{ }^\circ\text{C}$	—	1.65	—	V
		$V_{GE} = 15\text{ V}, I_C = 30\text{ A}$ $T_C = 125\text{ }^\circ\text{C}$	—	1.9	—	
Diode Forward On-Voltage	V_F	$I_F = 30\text{ A}, T_C = 25\text{ }^\circ\text{C}$	—	1.8	—	V
		$I_F = 30\text{ A}, T_C = 125\text{ }^\circ\text{C}$	—	1.5	—	V
Gate Threshold Voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}, I_C = 250\text{ }\mu\text{A}$	4.0	5.0	6.5	V
Collector Cut-off Current	I_{CES}	$V_{GE} = 0\text{ V}, V_{CE} = 600\text{ V}$	—	—	200	μA
Gate-Emitter Leakage Current	I_{GES}	$V_{CE} = 0\text{ V}, V_{GE} = \pm 20\text{ V}$	—	—	± 400	nA

Dynamic Electrical Characteristics ($T_C = 25\text{ }^\circ\text{C}$, Unless Otherwise Specified)

Parameter	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Input Capacitance	C_{ies}	$V_{CE} = 30\text{ V}, V_{GE} = 0\text{ V},$ $f = 1\text{ MHz}$	—	1650	—	pF
Output Capacitance	C_{oes}		—	130	—	
Reverse Transfer Capacitance	C_{res}		—	35	—	
Total Gate Charge	Q_g	$V_{CE} = 400\text{ V}, V_{GE} = 15\text{ V}$ $I_C = 30.0\text{ A}$	—	76	—	nC
Gate-Emitter Charge	Q_{ge}		—	20	—	
Gate-Collector Charge	Q_{gc}		—	38	—	

IGBT Switching Characteristics (Inductive Load, $T_C = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Turn-on Delay Time	$t_{d(on)}$	$V_{CE} = 400\text{ V}, V_{GE} = 15\text{ V}$ $I_C = 30.0\text{ A}, R_G = 10\text{ }\Omega$	—	30	—	ns
Current Rise Time	t_r		—	105	—	ns
Turn-off Delay Time	$t_{d(off)}$		—	67	—	ns
Current Fall Time	t_f		—	100	—	ns
Turn-on Switching Energy	E_{on}		—	1.85	—	mJ
Turn-off Switching Energy	E_{off}		—	0.45	—	mJ
Total Switching Energy	E_{ts}		—	2.3	—	mJ

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BIDW30N60T Insulated Gate Bipolar Transistor (IGBT)

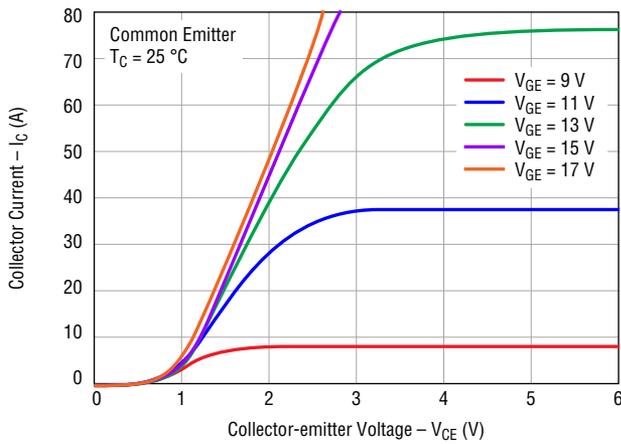


Diode Switching Characteristics ($T_C = 25\text{ }^\circ\text{C}$, unless otherwise specified)

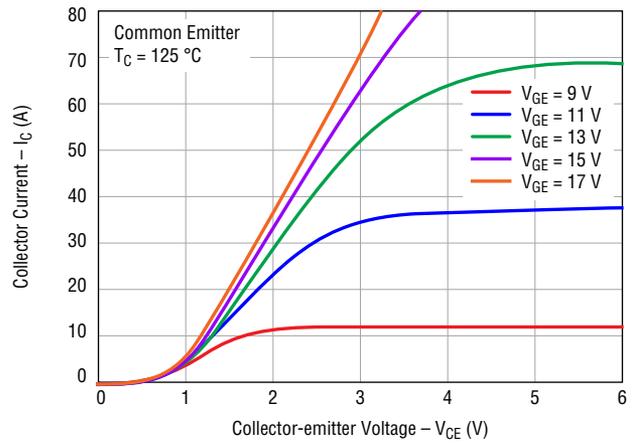
Parameter	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Reverse Recovery Time	t_{rr}	$di_F/dt = 200\text{ A}/\mu\text{s}$ $I_F = 30.0\text{ A}$	—	40	—	ns
Reverse Recovery Charge	Q_{rr}		—	90	—	nC

Electrical Characteristic Performance

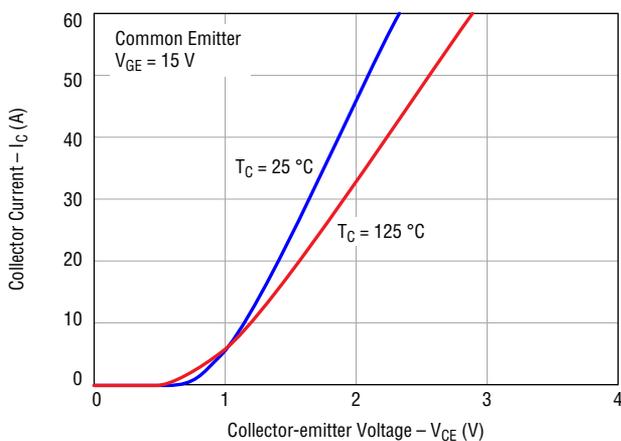
Typical Output Characteristics



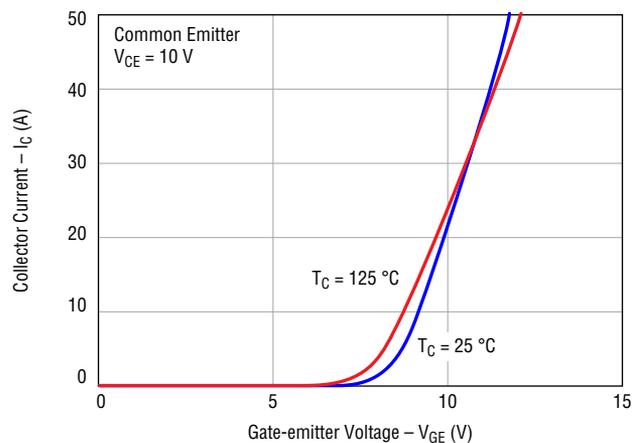
Typical Output Characteristics



Typical Saturation Voltage Characteristics



Typical Transfer Characteristics



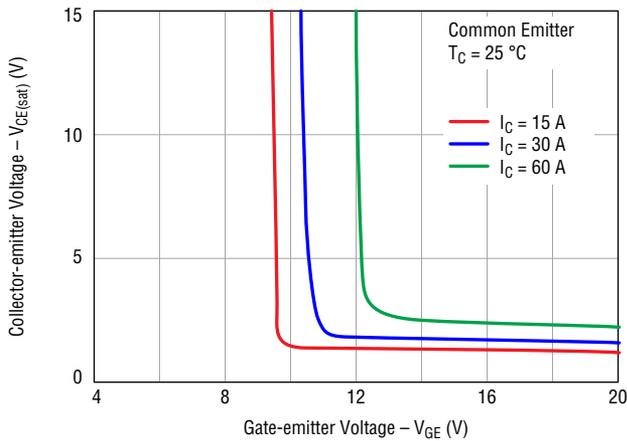
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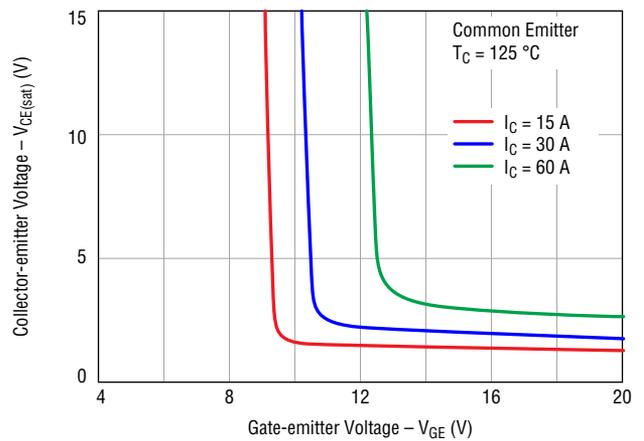
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Electrical Characteristic Performance (continued)

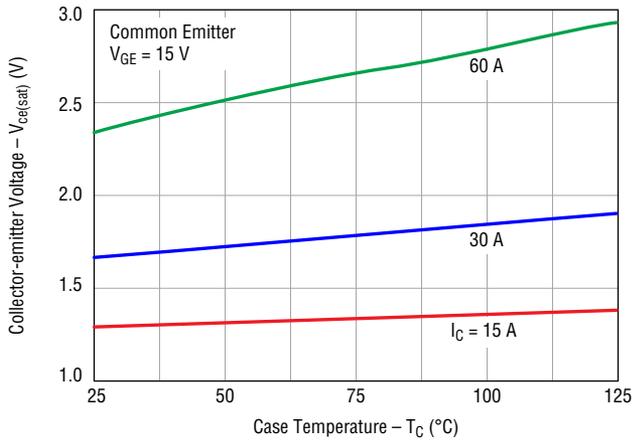
Typical $V_{CE(sat)}$ vs V_{GE} @ $T_C = 25^\circ\text{C}$



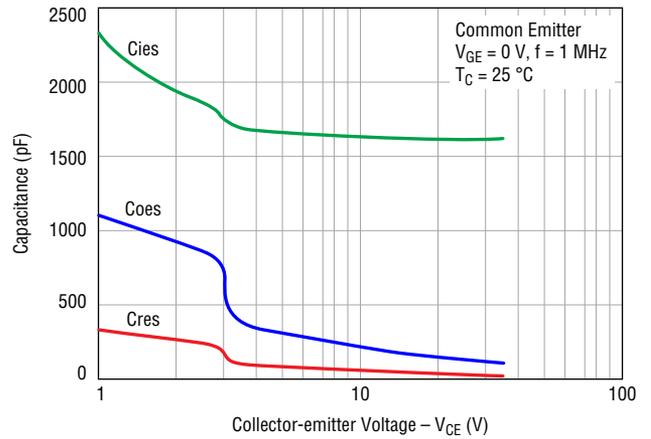
Typical $V_{CE(sat)}$ vs V_{GE} @ $T_C = 125^\circ\text{C}$



Typical $V_{CE(sat)}$ vs Case Temperature



Typical Capacitance Characteristics



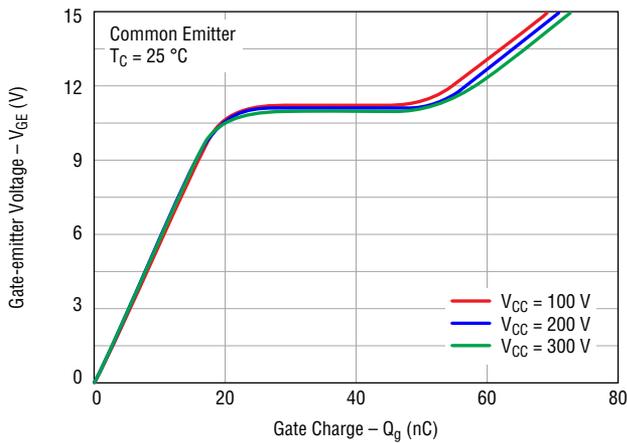
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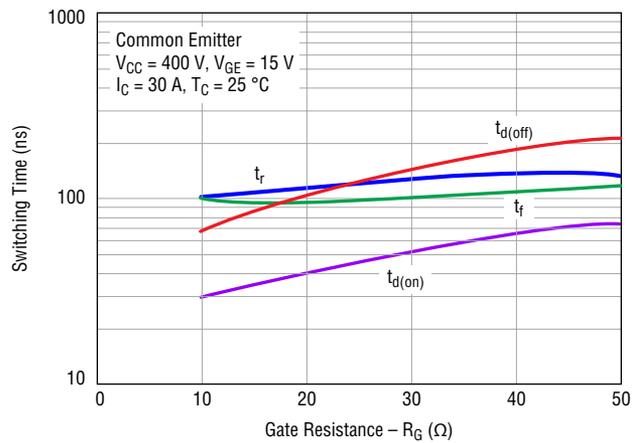
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Electrical Characteristic Performance (continued)

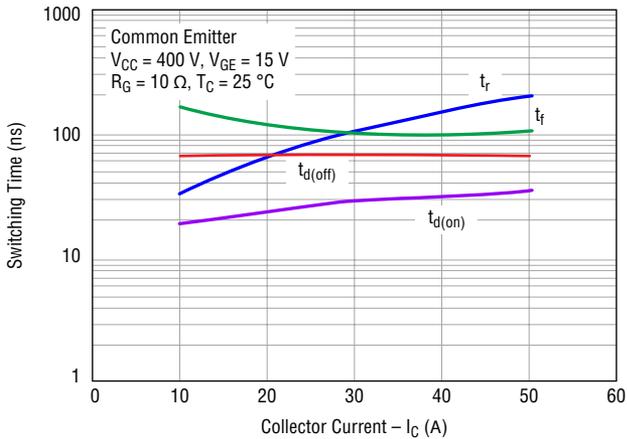
Typical Gate Charge Characteristics



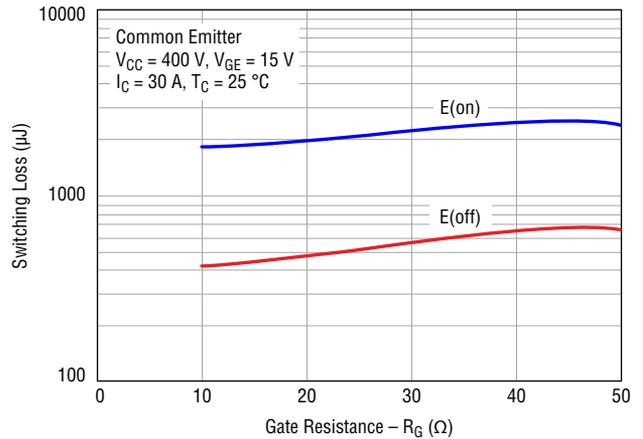
Typical Switching Time Characteristics vs R_G



Typical Switching Time Characteristics vs I_C



Typical Switching Loss vs R_G



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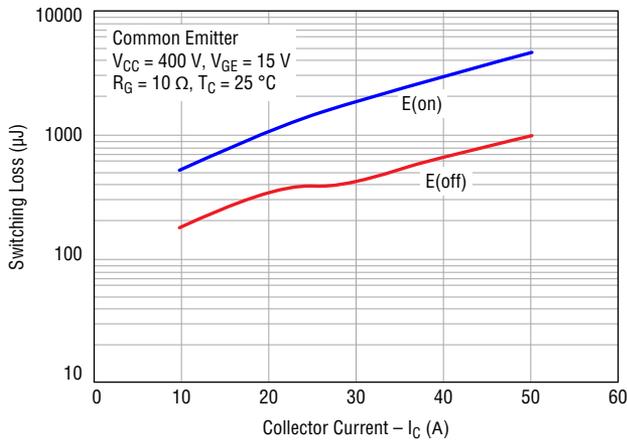
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BIDW30N60T Insulated Gate Bipolar Transistor (IGBT)

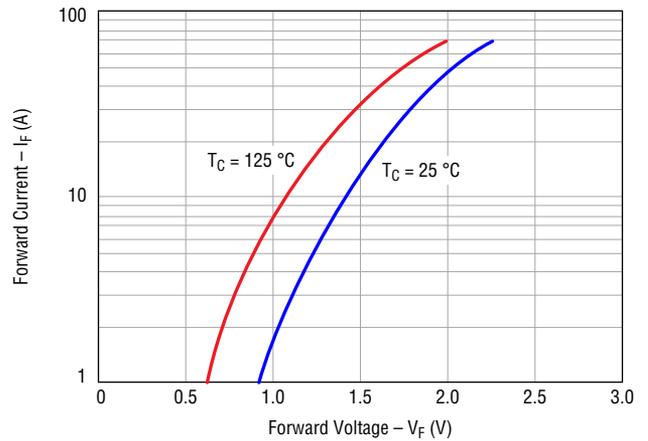


Electrical Characteristic Performance (continued)

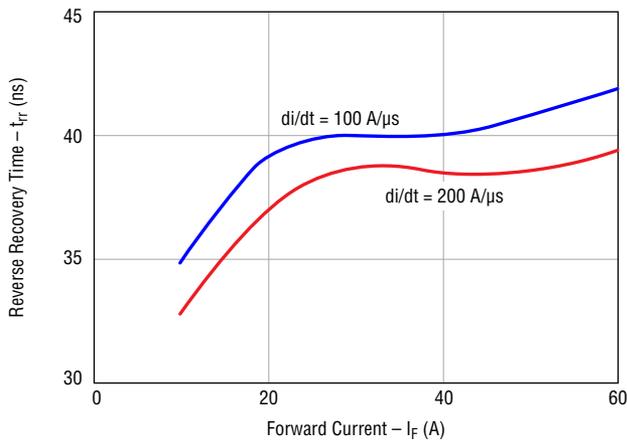
Typical Switching Loss Characteristics vs I_C



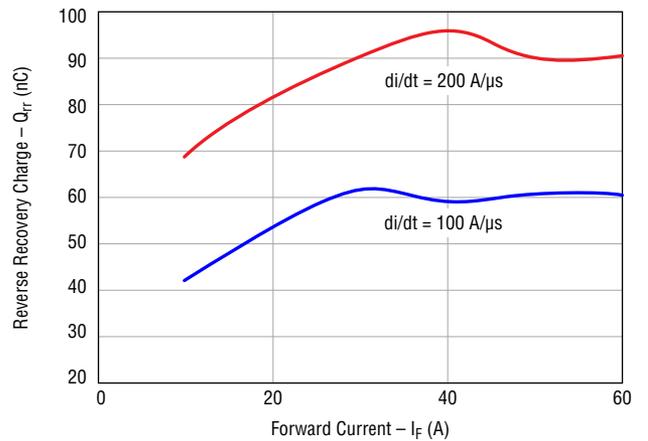
Typical Diode I_F vs V_F



Typical Reverse Recovery Time vs I_F



Typical Reverse Recovery Charge vs I_F



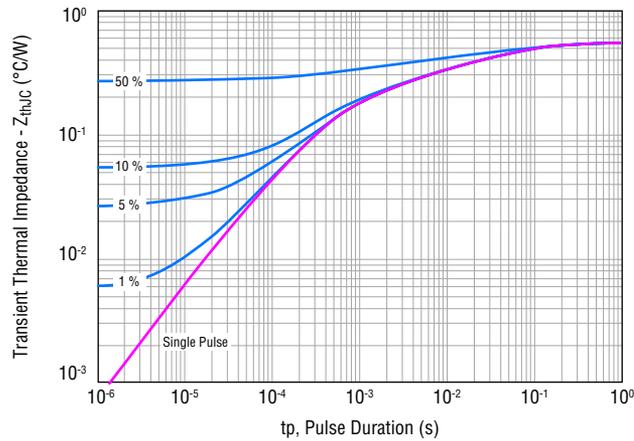
Specifications are subject to change without notice.

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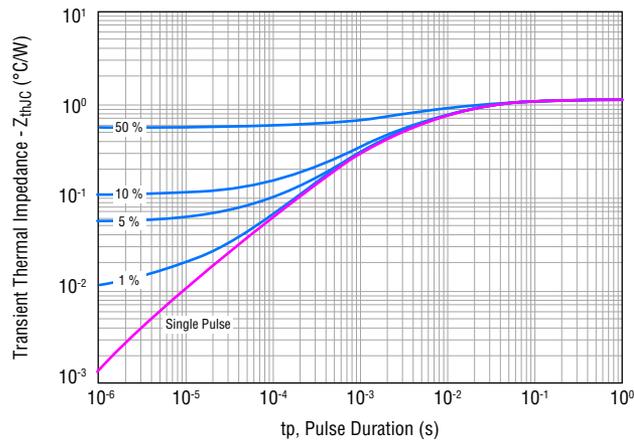
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Electrical Characteristic Performance (continued)

IGBT Transient Thermal Impedance vs $t_{p(on)}$ Duration ($D=t_p/T$)



Diode Transient Thermal Impedance vs $t_{p(on)}$ Duration ($D=t_p/T$)



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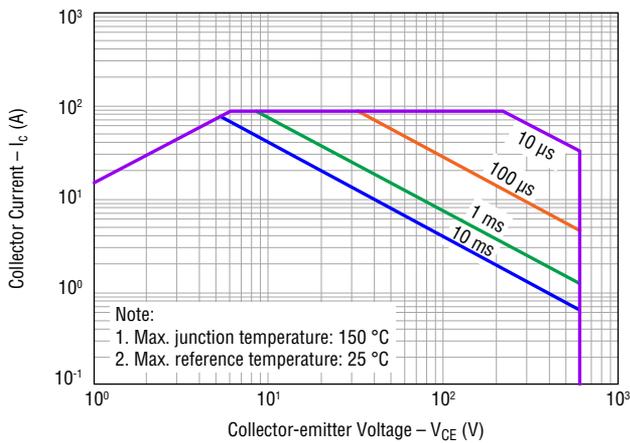
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BIDW30N60T Insulated Gate Bipolar Transistor (IGBT)



Electrical Characteristic Performance (continued)

Forward Bias Safe Operating Area

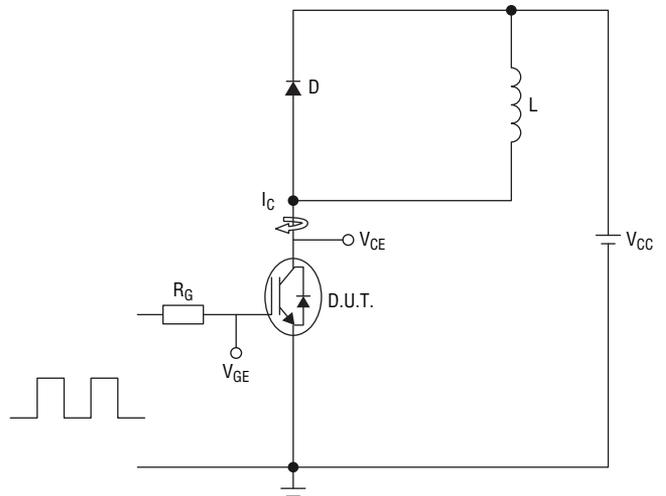


How to Order

B I D W 30 N 60 T

- B = Bourns®
- I = IGBT
- Type
D = Discrete
- Package Code
W = TO-247
- Current Rating
30 = 30 A
- Device Type
N = N-channel
- Nominal Voltage (divided by 10)
60 = 600 V
- Optimization
T = Medium Speed

Inductive Load Test Circuit



$L = 1.87 \text{ mH}$, $V_{CE} = 400 \text{ V}$, $V_{GE} = 15 \text{ V}$, $I_C = 30 \text{ A}$, $R_G = 10 \Omega$

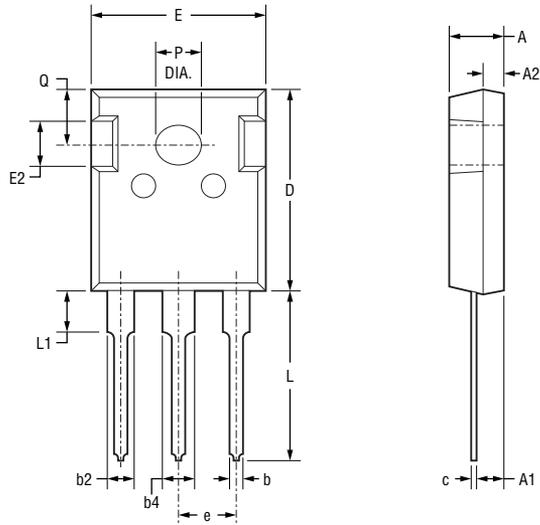
Environmental Characteristics

ESD Class (HBM) 2

BIDW30N60T Insulated Gate Bipolar Transistor (IGBT)

BOURNS®

Product Dimensions



DIMENSIONS: $\frac{\text{MM}}{\text{(INCHES)}}$

Symbol	Min.	Nom.	Max.
A	$\frac{4.80}{(.189)}$	$\frac{5.00}{(.197)}$	$\frac{5.20}{(.205)}$
A1	$\frac{2.21}{(.087)}$	$\frac{2.41}{(.095)}$	$\frac{2.59}{(.102)}$
A2	$\frac{1.85}{(.073)}$	$\frac{2.00}{(.079)}$	$\frac{2.15}{(.085)}$
b	$\frac{1.11}{(.044)}$	—	$\frac{1.36}{(.054)}$
b2	$\frac{1.91}{(.075)}$	—	$\frac{2.25}{(.089)}$
b4	$\frac{2.91}{(.115)}$	—	$\frac{3.25}{(.128)}$
c	$\frac{0.51}{(.020)}$	—	$\frac{0.75}{(.030)}$
D	$\frac{20.80}{(.819)}$	$\frac{21.00}{(.827)}$	$\frac{21.30}{(.839)}$
E	$\frac{15.50}{(.610)}$	$\frac{15.80}{(.622)}$	$\frac{16.10}{(.634)}$
E2	$\frac{4.40}{(.173)}$	$\frac{5.00}{(.197)}$	$\frac{5.20}{(.205)}$
e	$\frac{5.44}{(.214)}$ BSC		
L	$\frac{19.72}{(.776)}$	$\frac{19.92}{(.784)}$	$\frac{20.22}{(.796)}$
L1	—	—	$\frac{4.30}{(.169)}$
P	$\frac{3.40}{(.134)}$	—	$\frac{3.80}{(.150)}$
Q	$\frac{5.60}{(.220)}$	$\frac{5.80}{(.228)}$	$\frac{6.00}{(.236)}$

Packaging Specifications

BIDW30N60T 30 pieces per tube

BOURNS®

Asia-Pacific: Tel: +886-2 2562-4117

Email: asiacus@bourns.com

EMEA: Tel: +36 88 885 877

Email: eurocus@bourns.com

The Americas: Tel: +1-951 781-5500

Email: americus@bourns.com

www.bourns.com

07/22

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Web Page: <http://www.bourns.com/legal/disclaimers-terms-and-policies>

PDF: <http://www.bourns.com/docs/Legal/disclaimer.pdf>



Features

- 600 V, 30 A, Low Collector-Emitter Saturation Voltage ($V_{CE(sat)}$)
- Trench-Gate Field-Stop technology
- Low switching loss
- Fast switching
- RoHS compliant*

Applications

- Switch-Mode Power Supplies (SMPS)
- Uninterruptible Power Sources (UPS)
- Power Factor Correction (PFC)
- Induction heating

BIDNW30N60H3 Insulated Gate Bipolar Transistor (IGBT)

General Information

The Bourns® Model BIDNW30N60H3 IGBT device combines technology from a MOS gate and a bipolar transistor for an optimum component for high voltage and high current applications. This device uses Trench-Gate Field-Stop technology providing greater control of dynamic characteristics with a lower Collector-Emitter Saturation Voltage ($V_{CE(sat)}$) and fewer switching losses.

Additional Information

Click these links for more information:



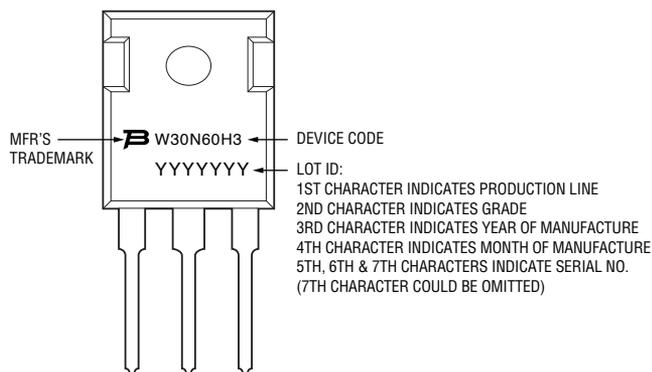
Maximum Electrical Ratings ($T_C = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CES}	600	V
Continuous Collector Current ($T_C = 25\text{ }^\circ\text{C}$), limited by T_{jmax}	I_C	60	A
Continuous Collector Current ($T_C = 100\text{ }^\circ\text{C}$), limited by T_{jmax}	I_C	30	A
Pulsed Collector Current, t_p limited by T_{jmax}	I_{CP}	120	A
Gate-Emitter Voltage	V_{GE}	± 20	V
Continuous Forward Current ($T_C = 100\text{ }^\circ\text{C}$), limited by T_{jmax}	I_F	12	A
Total Power Dissipation	P_{total}	230	W
Storage Temperature	T_{STG}	-55 to +150	$^\circ\text{C}$
Operating Junction Temperature	T_j	-55 to +150	$^\circ\text{C}$

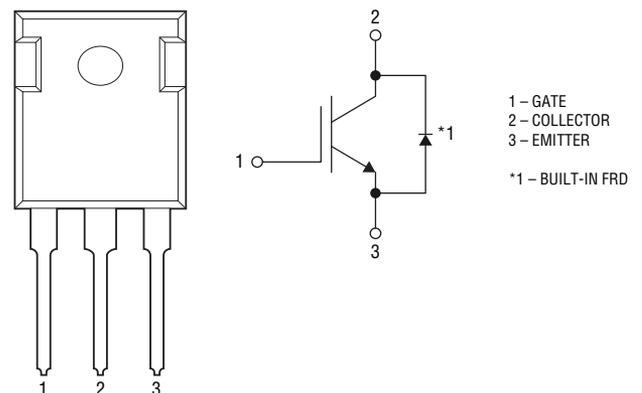
Thermal Resistance

Parameter	Symbol	Max	Unit
IGBT Thermal Resistance Junction - Case	$R_{th(j-c)}_{IGBT}$	0.54	$^\circ\text{C/W}$
Diode Thermal Resistance Junction - Case	$R_{th(j-c)}_{Diode}$	1.5	$^\circ\text{C/W}$

Typical Part Marking



Internal Circuit



*RoHS Directive 2015/863, Mar 31, 2015 and Annex. Specifications are subject to change without notice. Users should verify actual device performance in their specific applications. The products described herein and this document are subject to specific legal disclaimers as set forth on the last page of this document, and at www.bourns.com/docs/legal/disclaimer.pdf.

BIDNW30N60H3 Insulated Gate Bipolar Transistor (IGBT)

BOURNS®

Static Electrical Characteristics ($T_C = 25\text{ }^\circ\text{C}$, Unless Otherwise Specified)

Parameter	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Collector-Emitter Breakdown Voltage	BV_{CES}	$V_{GE} = 0\text{ V}, I_C = 250\text{ }\mu\text{A}$	600	—	—	V
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	$V_{GE} = 15\text{ V}, I_C = 30\text{ A}$ $T_C = 25\text{ }^\circ\text{C}$	—	1.65	2.0	V
		$V_{GE} = 15\text{ V}, I_C = 30\text{ A}$ $T_C = 125\text{ }^\circ\text{C}$	—	1.9	—	
Diode Forward On-Voltage	V_F	$I_F = 12\text{ A}, T_C = 25\text{ }^\circ\text{C}$	—	1.8	—	V
		$I_F = 12\text{ A}, T_C = 125\text{ }^\circ\text{C}$	—	1.4	—	V
Gate Threshold Voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}, I_C = 250\text{ }\mu\text{A}$	4.0	5.0	6.5	V
Collector Cut-off Current	I_{CES}	$V_{GE} = 0\text{ V}, V_{CE} = 600\text{ V}$	—	—	200	μA
Gate-Emitter Leakage Current	I_{GES}	$V_{CE} = 0\text{ V}, V_{GE} = \pm 20\text{ V}$	—	—	± 400	nA

Dynamic Electrical Characteristics ($T_C = 25\text{ }^\circ\text{C}$, Unless Otherwise Specified)

Parameter	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Input Capacitance	C_{ies}	$V_{CE} = 30\text{ V}, V_{GE} = 0\text{ V},$ $f = 1\text{ MHz}$	—	1780	—	pF
Output Capacitance	C_{oes}		—	100	—	
Reverse Transfer Capacitance	C_{res}		—	32	—	
Total Gate Charge	Q_g	$V_{CE} = 400\text{ V}, V_{GE} = 15\text{ V}$ $I_C = 30.0\text{ A}$	—	76	—	nC
Gate-Emitter Charge	Q_{ge}		—	20	—	
Gate-Collector Charge	Q_{gc}		—	38	—	

IGBT Switching Characteristics (Inductive Load, $T_C = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter ($T_C = 25\text{ }^\circ\text{C}$)	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Turn-on Delay Time	$t_{d(on)}$	$V_{CE} = 400\text{ V}, V_{GE} = 15\text{ V}$ $I_C = 30.0\text{ A}, R_G = 10\text{ }\Omega$	—	30	—	ns
Current Rise Time	t_r		—	105	—	ns
Turn-off Delay Time	$t_{d(off)}$		—	67	—	ns
Current Fall Time	t_f		—	100	—	ns
Turn-on Switching Energy	E_{on}		—	1.85	—	mJ
Turn-off Switching Energy	E_{off}		—	0.45	—	mJ
Total Switching Energy	E_{ts}		—	2.3	—	mJ

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BIDNW30N60H3 Insulated Gate Bipolar Transistor (IGBT)

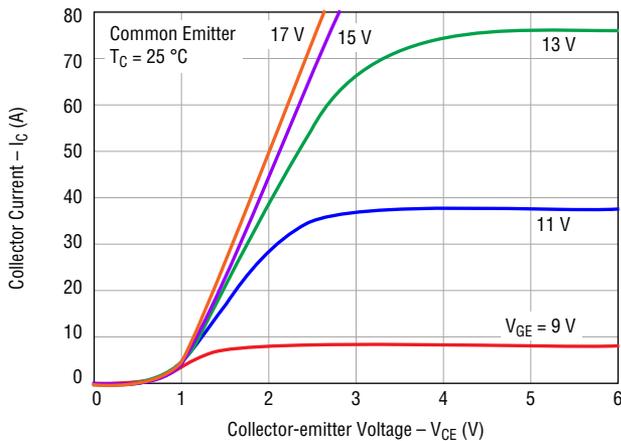
BOURNS®

Diode Switching Characteristics ($T_C = 25\text{ }^\circ\text{C}$, unless otherwise specified)

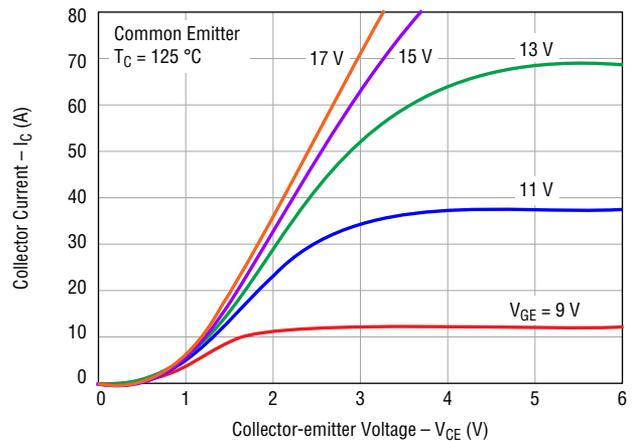
Parameter	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Reverse Recovery Time	t_{rr}	$di_F/dt = 200\text{ A}/\mu\text{s}$ $I_F = 12.0\text{ A}$	—	28	—	ns
Reverse Recovery Charge	Q_{rr}		—	55	—	nC

Electrical Characteristic Performance

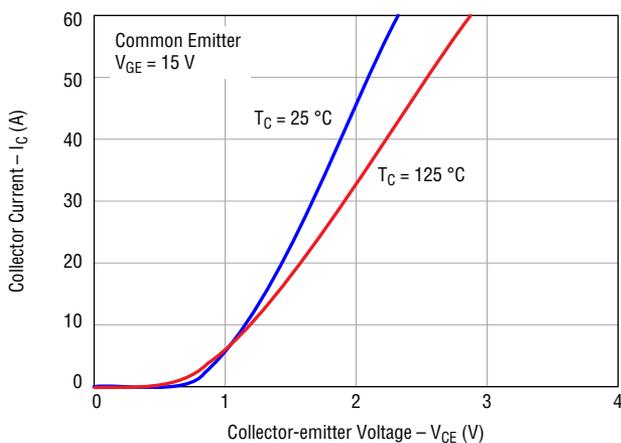
Typical Output Characteristics



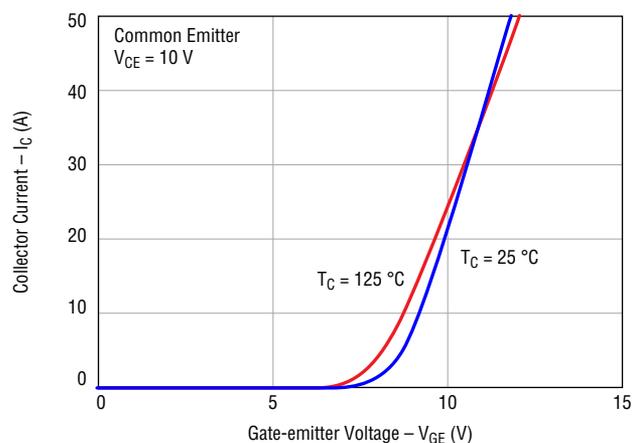
Typical Output Characteristics



Typical Saturation Voltage Characteristics



Typical Transfer Characteristics



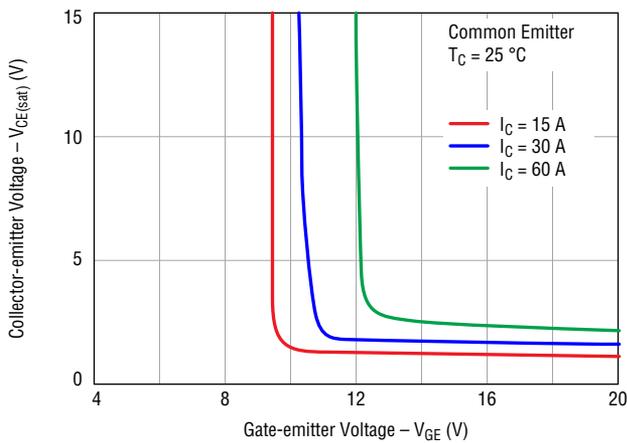
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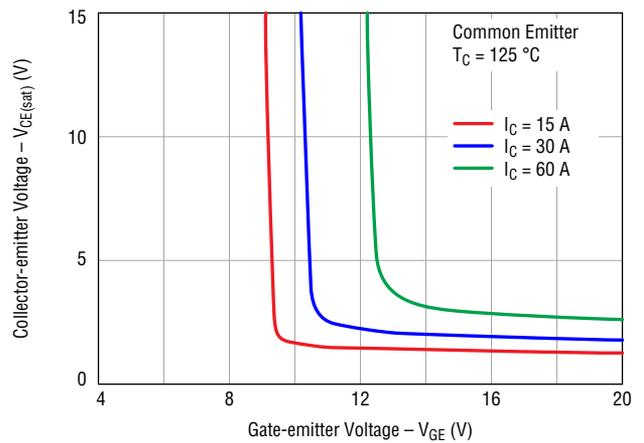
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Electrical Characteristic Performance (continued)

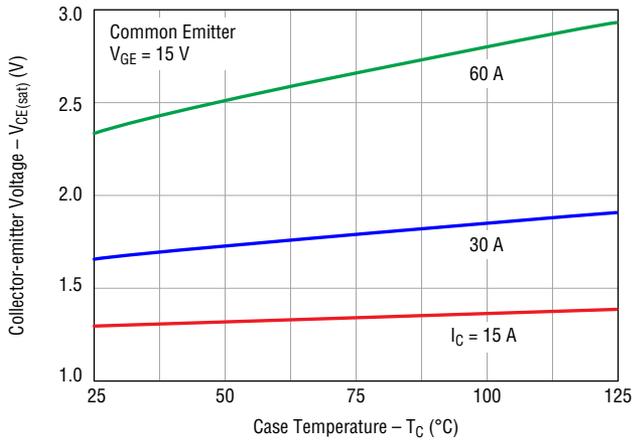
Typical $V_{CE(sat)}$ vs V_{GE} @ $T_C = 25^\circ\text{C}$



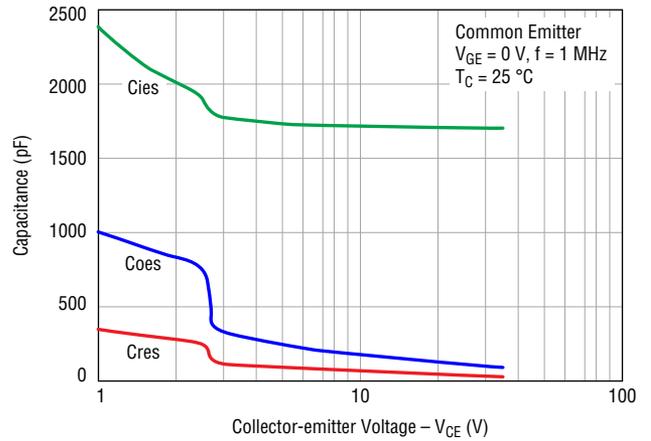
Typical $V_{CE(sat)}$ vs V_{GE} @ $T_C = 125^\circ\text{C}$



Typical $V_{CE(sat)}$ vs Case Temperature



Typical Capacitance Characteristics



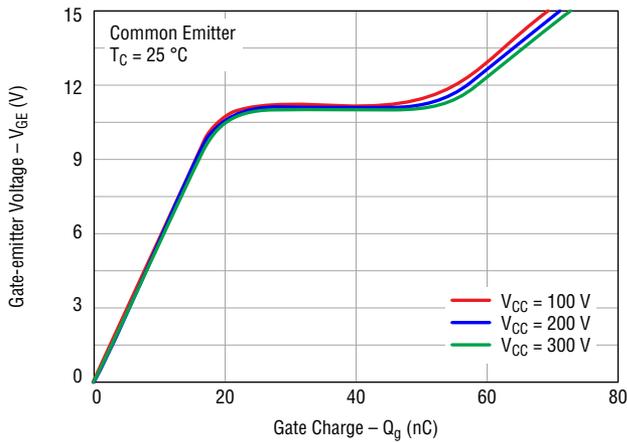
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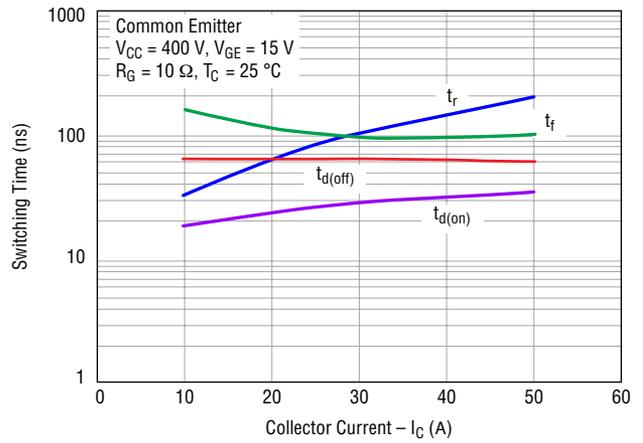
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Electrical Characteristic Performance (continued)

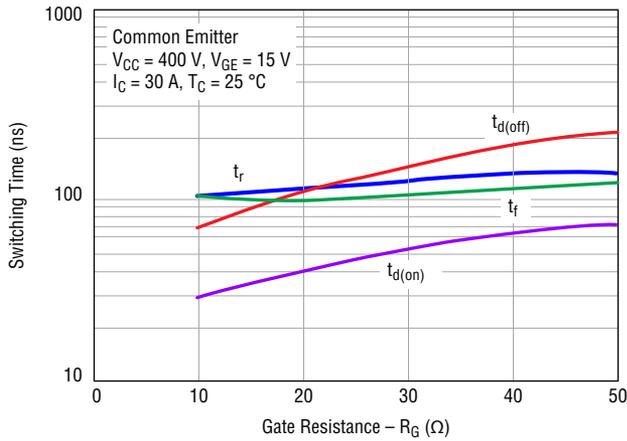
Typical Gate Charge Characteristic



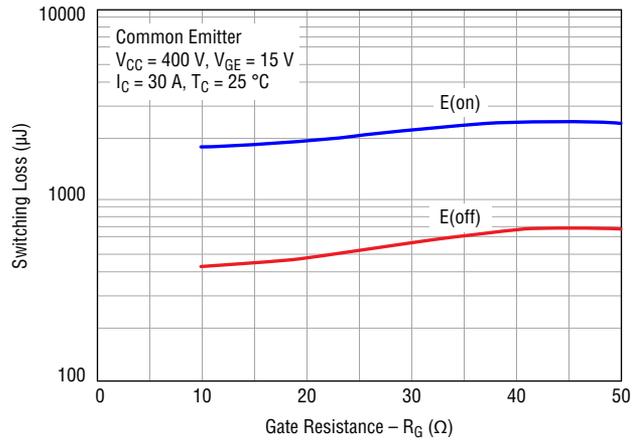
Typical Switching Time Characteristics vs I_C



Typical Switching Time Characteristics vs R_G



Typical Switching Loss vs R_G



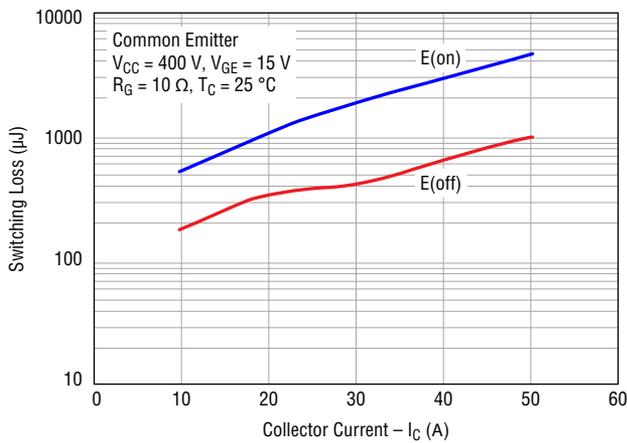
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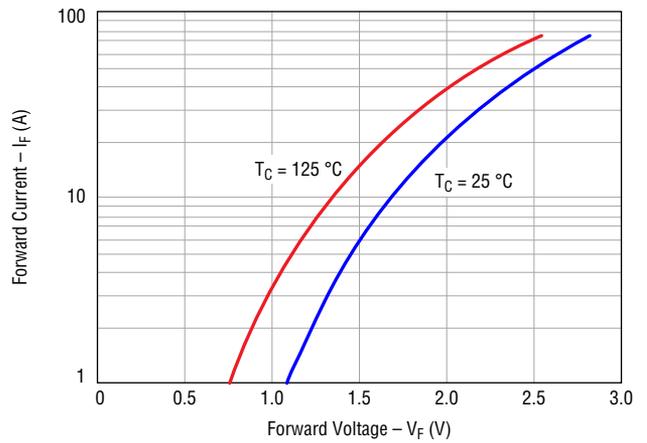
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Electrical Characteristic Performance (continued)

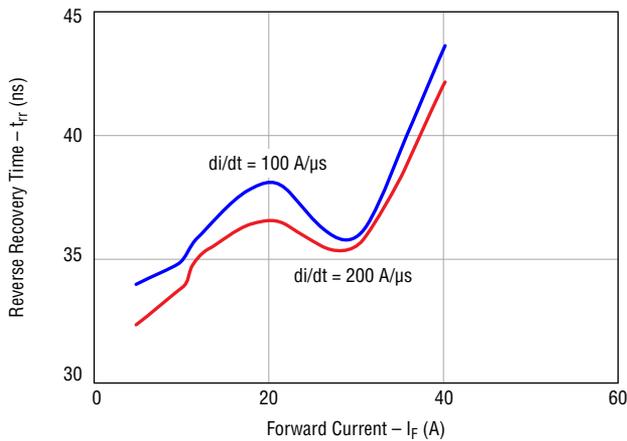
Typical Switching Loss Characteristics vs I_C



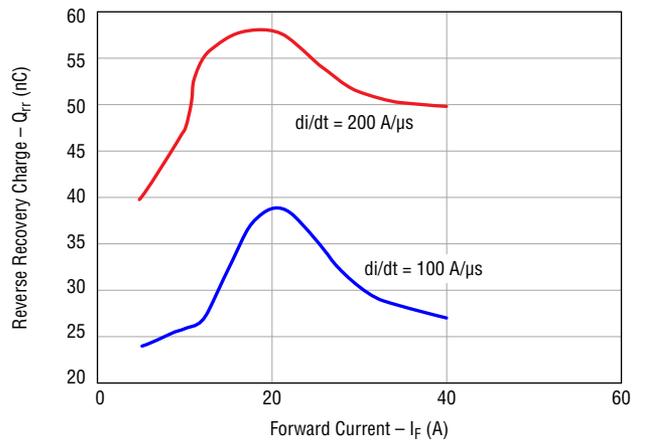
Typical Diode I_F vs V_F



Typical Reverse Recovery Time vs I_F



Typical Reverse Recovery Charge vs I_F



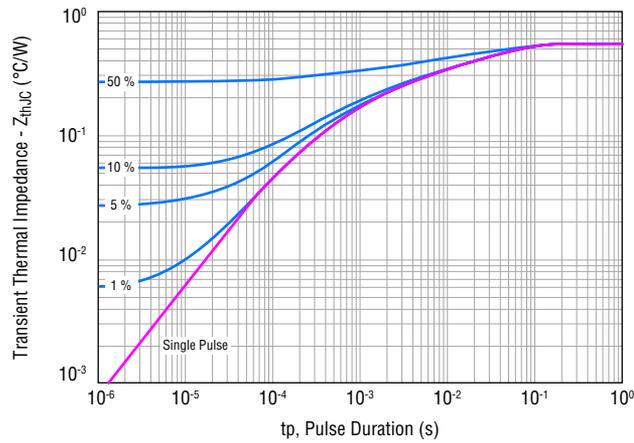
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Users should verify actual device performance in their specific applications.

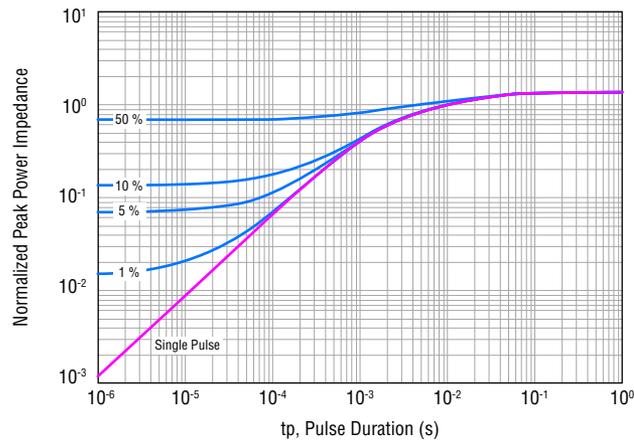
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Electrical Characteristic Performance (continued)

IGBT Transient Thermal Impedance vs $t_{p(on)}$ Duration ($D=t_p/T$)



Diode Transient Thermal Impedance vs $t_{p(on)}$ Duration ($D=t_p/T$)



Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

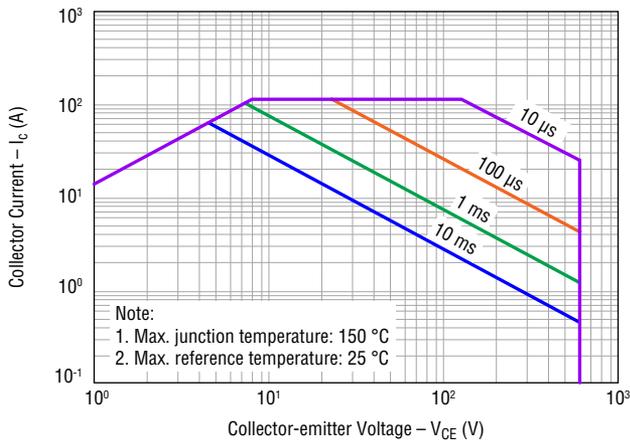
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BIDNW30N60H3 Insulated Gate Bipolar Transistor (IGBT)



Electrical Characteristic Performance (continued)

Forward Bias Safe Operating Area

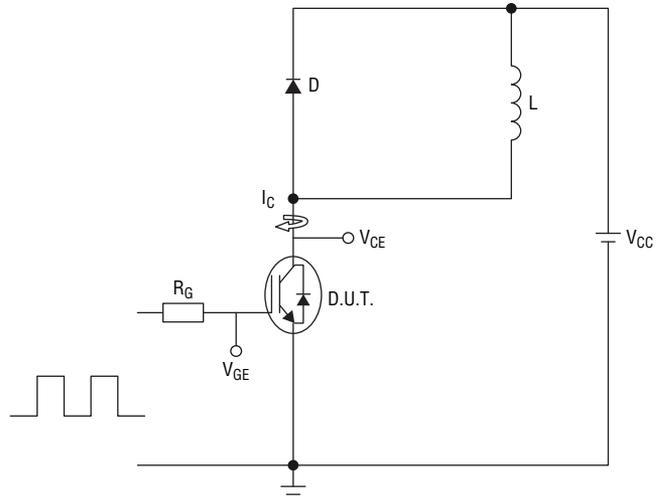


How to Order

B I D N W 30 N 60 H 3

- B = Bourns®
- I = IGBT
- Type
D = Discrete
- Packaging Code
NW = TO-247N-3L
- Current Rating
30 = 30 A
- Device Type
N = N-channel
- Nominal Voltage (divided by 10)
60 = 600 V
- Optimization
H = High Speed
- Version Number

Inductive Load Test Circuit



$L = 1.87 \text{ mH}$, $V_{CE} = 400 \text{ V}$, $V_{GE} = 15 \text{ V}$, $I_C = 30 \text{ A}$, $R_G = 10 \Omega$

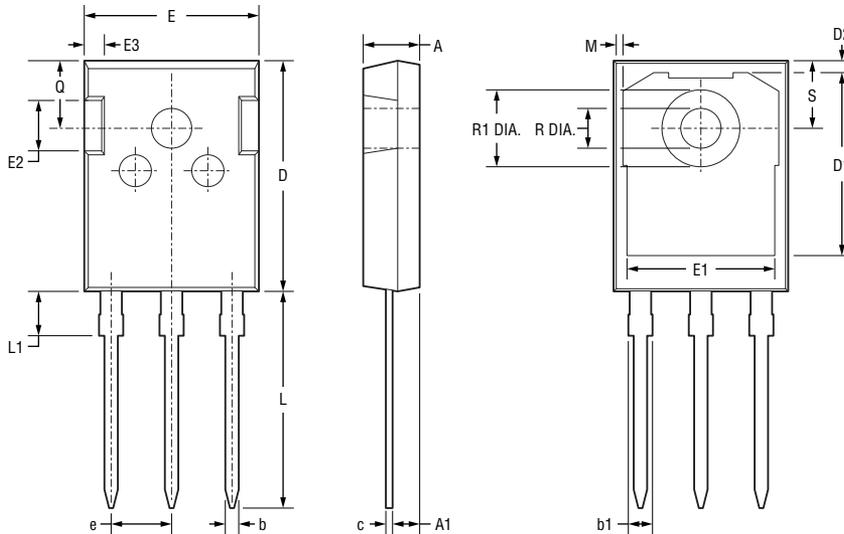
Environmental Characteristics

ESD Class (HBM)2

BIDNW30N60H3 Insulated Gate Bipolar Transistor (IGBT)



Product Dimensions



DIMENSIONS: $\frac{\text{MM}}{\text{(INCHES)}}$

Symbol	Min.	Nom.	Max.
A	4.90 (.193)	5.00 (.197)	5.10 (.201)
A1	2.31 (.091)	2.41 (.095)	2.51 (.099)
b	1.16 (.046)	—	1.26 (.050)
b1	—	—	2.25 (.089)
c	0.59 (.023)	—	0.66 (.026)
D	20.90 (.823)	21.00 (.827)	21.10 (.831)
D1	16.25 (.640)	16.55 (.652)	16.85 (.663)
D2	1.05 (.041)	1.17 (.046)	1.35 (.053)
E	15.70 (.618)	15.80 (.622)	15.90 (.626)
E1	13.10 (.516)	13.30 (.524)	13.50 (.531)
E2	4.40 (.173)	4.50 (.177)	4.60 (.181)
E3	1.50 (.059)	1.60 (.063)	1.70 (.067)
e	5.436 (.214) BSC		
L	19.80 (.780)	19.92 (.784)	20.10 (.791)
L1	—	—	4.30 (.169)
M	0.35 (.014)	—	0.95 (.037)
R	3.40 (.134)	3.50 (.138)	3.60 (.142)
R1	7.00 (.276)	—	7.40 (.291)
Q	5.60 (.220)	—	6.00 (.236)
S	6.05 (.238)	6.15 (.242)	6.25 (.246)

Packaging Specifications

BIDNW30N60H3 30 pieces per tube



Asia-Pacific: Tel: +886-2 2562-4117 • Email: asiacus@bourns.com

EMEA: Tel: +36 88 885 877 • Email: eurocus@bourns.com

The Americas: Tel: +1-951 781-5500 • Email: americus@bourns.com

www.bourns.com

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Specifications are subject to change without notice.

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Web Page: <http://www.bourns.com/legal/disclaimers-terms-and-policies>

PDF: <http://www.bourns.com/docs/Legal/disclaimer.pdf>



Features

- 650 V, 50 A, Low Collector-Emitter Saturation Voltage ($V_{CE(sat)}$)
- Trench-Gate Field-Stop technology
- Optimized for conduction
- RoHS compliant*

Applications

- Switch-Mode Power Supplies (SMPS)
- Uninterruptible Power Sources (UPS)
- Power Factor Correction (PFC)
- Inverters

BIDW50N65T Insulated Gate Bipolar Transistor (IGBT)

General Information

The Bourns® Model BIDW50N65T IGBT device combines technology from a MOS gate and a bipolar transistor for an optimum component for high voltage and high current applications. This device uses Trench-Gate Field-Stop technology providing greater control of dynamic characteristics with a lower Collector-Emitter Saturation Voltage ($V_{CE(sat)}$) and fewer switching losses. In addition, this structure provides a lower thermal resistance R_{th} .

Additional Information

Click these links for more information:



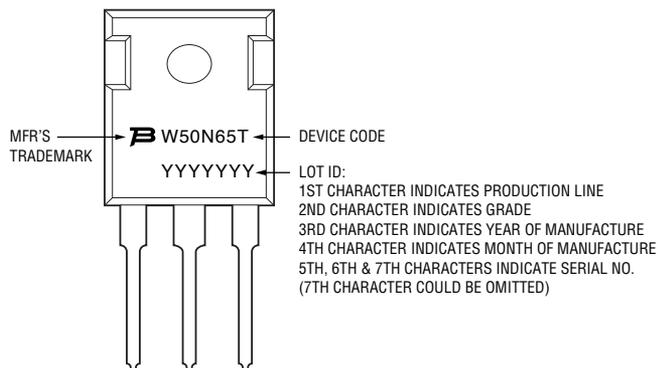
Maximum Electrical Ratings ($T_C = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CES}	650	V
Continuous Collector Current ($T_C = 25\text{ }^\circ\text{C}$), limited by T_{jmax}	I_C	100	A
Continuous Collector Current ($T_C = 100\text{ }^\circ\text{C}$), limited by T_{jmax}	I_C	50	A
Pulsed Collector Current, t_p limited by T_{jmax}	I_{CP}	150	A
Gate-Emitter Voltage	V_{GE}	± 20	V
Continuous Forward Current ($T_C = 100\text{ }^\circ\text{C}$), limited by T_{jmax}	I_F	50	A
Short-circuit Withstand Time ($V_{CE} = 300\text{ V}$, $V_{GE} = 15\text{ V}$)	T_{SC}	10	μs
Total Power Dissipation	P_{total}	416	W
Storage Temperature	T_{STG}	-55 to +150	$^\circ\text{C}$
Operating Junction Temperature	T_j	-55 to +150	$^\circ\text{C}$

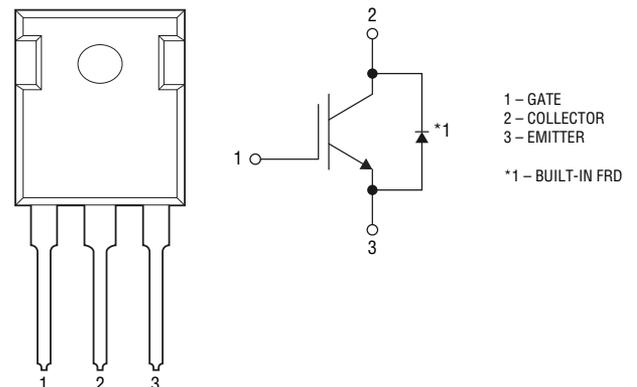
Thermal Resistance

Parameter	Symbol	Max	Unit
IGBT Thermal Resistance Junction - Case	$R_{th(j-c)}_{IGBT}$	0.3	$^\circ\text{C/W}$
Diode Thermal Resistance Junction - Case	$R_{th(j-c)}_{Diode}$	0.65	$^\circ\text{C/W}$

Typical Part Marking



Internal Circuit



WARNING Cancer and Reproductive Harm
www.P65Warnings.ca.gov

*RoHS Directive 2015/863, Mar 31, 2015 and Annex. Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

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BIDW50N65T Insulated Gate Bipolar Transistor (IGBT)

BOURNS®

Static Electrical Characteristics ($T_C = 25\text{ }^\circ\text{C}$, Unless Otherwise Specified)

Parameter	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Collector-Emitter Breakdown Voltage	BV_{CES}	$V_{GE} = 0\text{ V}, I_C = 250\text{ }\mu\text{A}$	650	—	—	V
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	$V_{GE} = 15\text{ V}, I_C = 50\text{ A}$ $T_C = 25\text{ }^\circ\text{C}$	—	1.65	2.2	V
		$V_{GE} = 15\text{ V}, I_C = 50\text{ A}$ $T_C = 125\text{ }^\circ\text{C}$	—	1.9	—	
Diode Forward On-Voltage	V_F	$I_F = 50\text{ A}, T_C = 25\text{ }^\circ\text{C}$	—	1.7	2.5	V
		$I_F = 50\text{ A}, T_C = 125\text{ }^\circ\text{C}$	—	1.3	—	V
Gate Threshold Voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}, I_C = 250\text{ }\mu\text{A}$	4.0	5.0	7.0	V
Collector Cut-off Current	I_{CES}	$V_{GE} = 0\text{ V}, V_{CE} = 650\text{ V}$	—	—	200	μA
Gate-Emitter Leakage Current	I_{GES}	$V_{CE} = 0\text{ V}, V_{GE} = \pm 20\text{ V}$	—	—	± 400	nA

Dynamic Electrical Characteristics ($T_C = 25\text{ }^\circ\text{C}$, Unless Otherwise Specified)

Parameter	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Input Capacitance	C_{ies}	$V_{CE} = 30\text{ V}, V_{GE} = 0\text{ V},$ $f = 1\text{ MHz}$	—	2723	—	pF
Output Capacitance	C_{oes}		—	230	—	
Reverse Transfer Capacitance	C_{res}		—	55	—	
Total Gate Charge	Q_g	$V_{CE} = 400\text{ V}, V_{GE} = 15\text{ V}$ $I_C = 50.0\text{ A}$	—	123	—	nC
Gate-Emitter Charge	Q_{ge}		—	31	—	
Gate-Collector Charge	Q_{gc}		—	48	—	

IGBT Switching Characteristics (Inductive Load, $T_C = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Turn-on Delay Time	$t_{d(on)}$	$V_{CE} = 400\text{ V}, V_{GE} = 15\text{ V}$ $I_C = 50.0\text{ A}, R_G = 10\text{ }\Omega$	—	37	—	ns
Current Rise Time	t_r		—	133	—	ns
Turn-off Delay Time	$t_{d(off)}$		—	125	—	ns
Current Fall Time	t_f		—	121	—	ns
Turn-on Switching Energy	E_{on}		—	3.0	—	mJ
Turn-off Switching Energy	E_{off}		—	1.1	—	mJ
Total Switching Energy	E_{ts}		—	4.1	—	mJ

Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

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BIDW50N65T Insulated Gate Bipolar Transistor (IGBT)

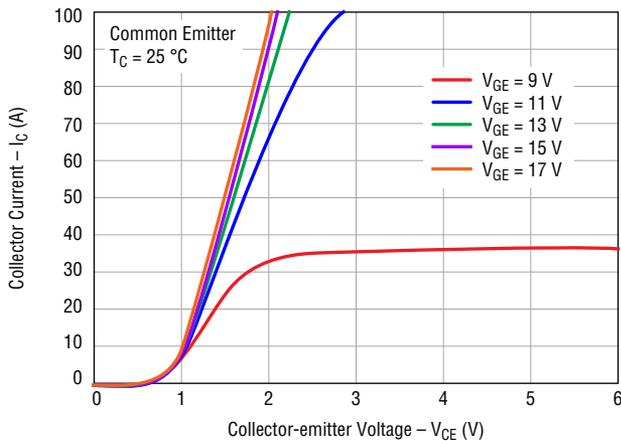
BOURNS®

Diode Switching Characteristics ($T_C = 25\text{ }^\circ\text{C}$, unless otherwise specified)

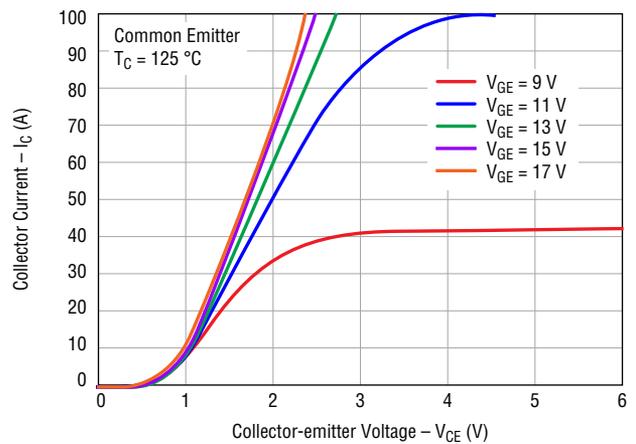
Parameter	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Reverse Recovery Time	t_{rr}	$di_F/dt = 200\text{ A}/\mu\text{s}$ $I_F = 50.0\text{ A}$	—	37.5	—	ns
Reverse Recovery Charge	Q_{rr}		—	78	—	nC

Electrical Characteristic Performance

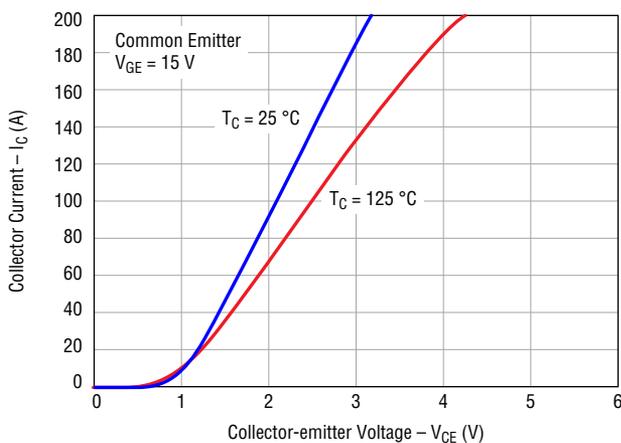
Typical Output Characteristics



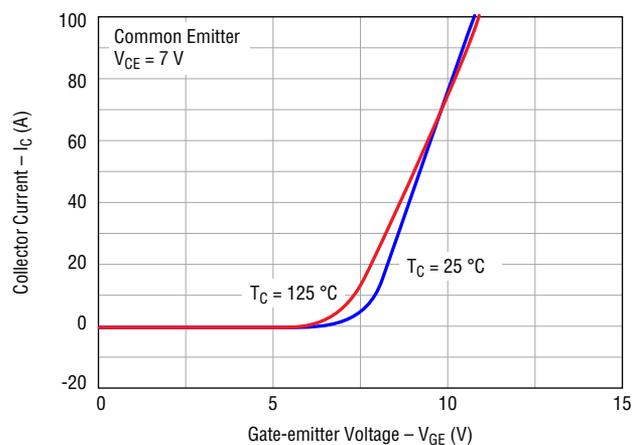
Typical Output Characteristics



Typical Saturation Voltage Characteristics



Typical Transfer Characteristics



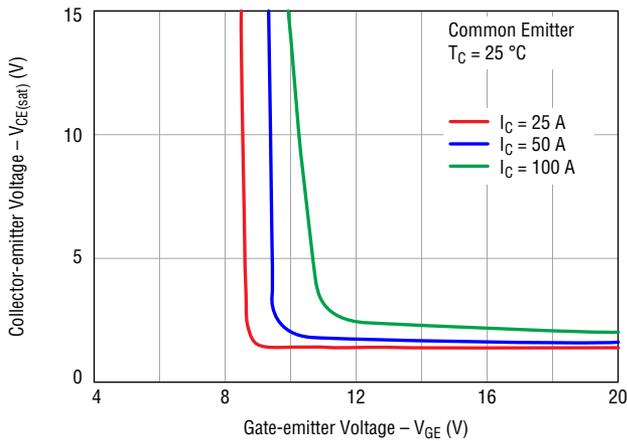
Specifications are subject to change without notice.

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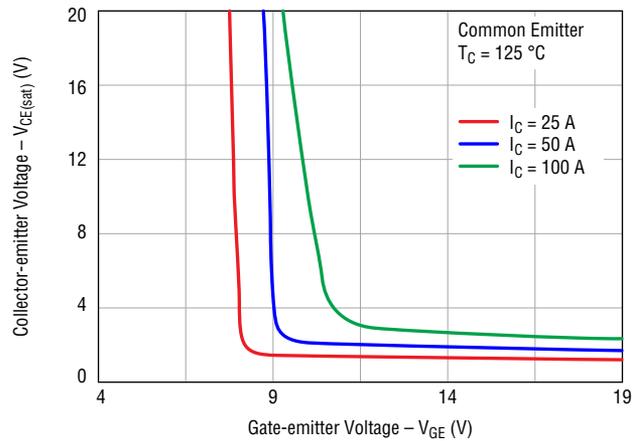
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Electrical Characteristic Performance (continued)

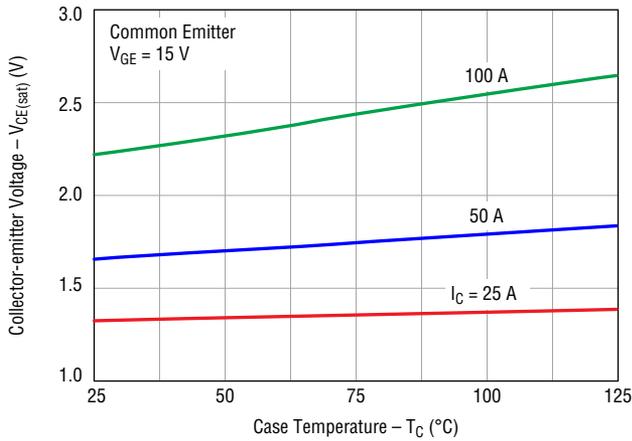
Typical $V_{CE(sat)}$ vs V_{GE} @ $T_C = 25^\circ\text{C}$



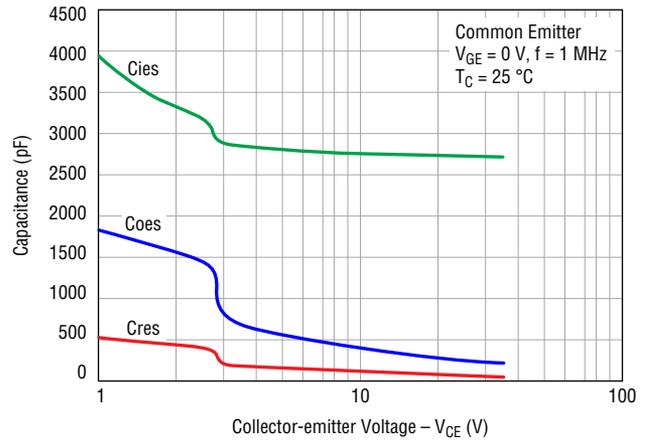
Typical $V_{CE(sat)}$ vs V_{GE} @ $T_C = 125^\circ\text{C}$



Typical $V_{CE(sat)}$ vs Case Temperature



Typical Capacitance Characteristics



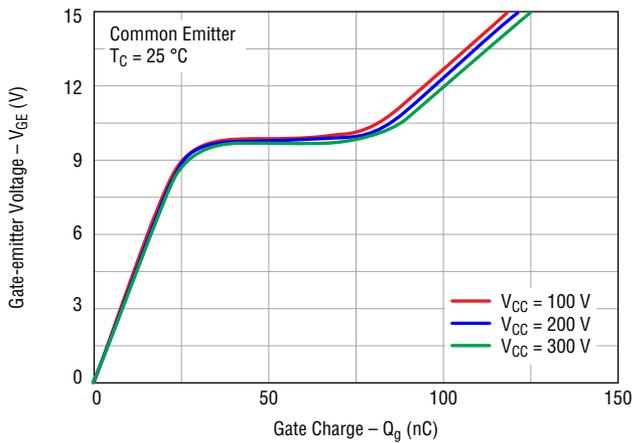
Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

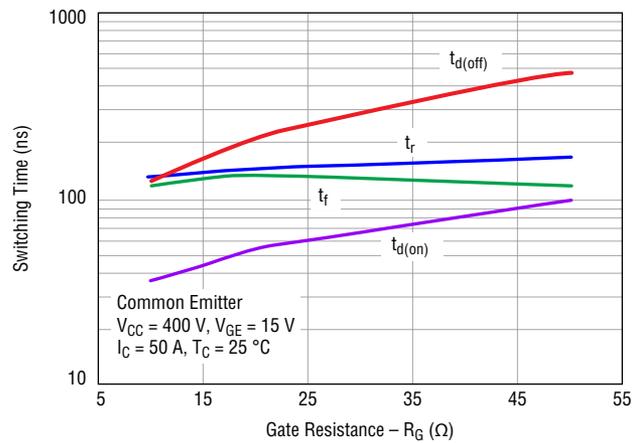
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Electrical Characteristic Performance (continued)

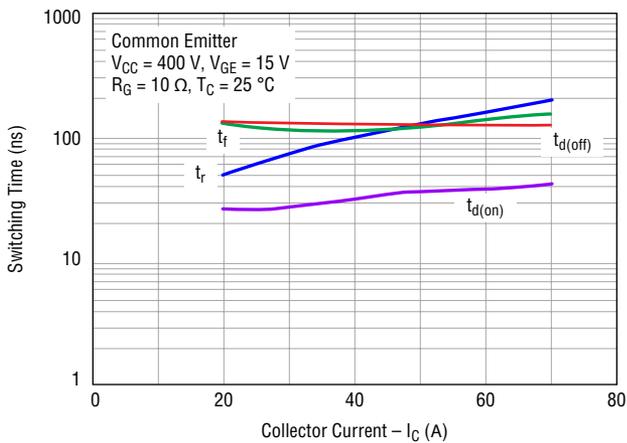
Typical Gate Charge Characteristics



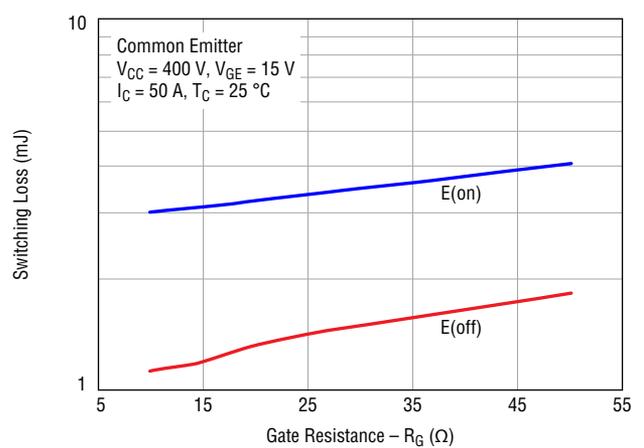
Typical Switching Time Characteristics vs R_G



Typical Switching Time Characteristics vs I_C



Typical Switching Loss vs R_G



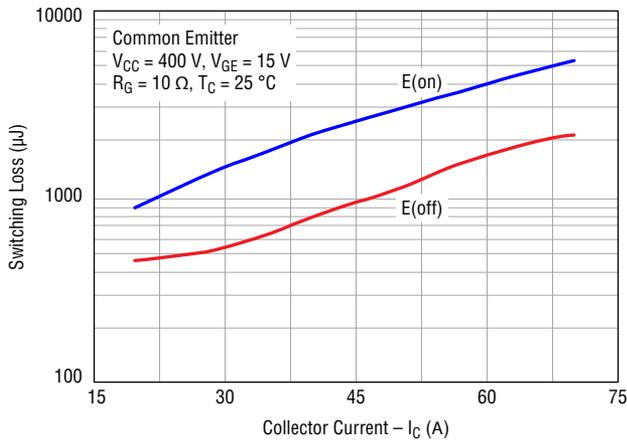
Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

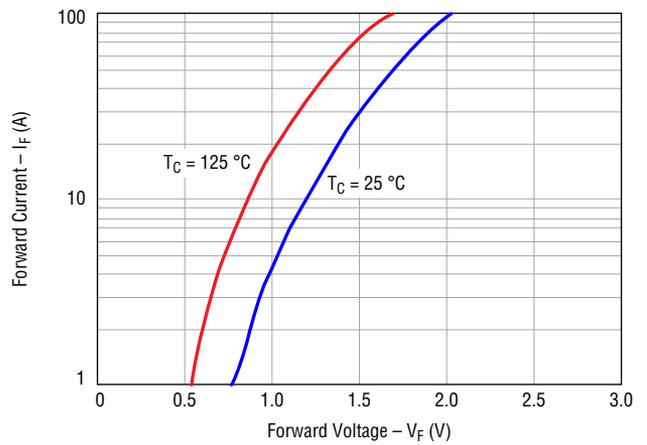
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Electrical Characteristic Performance (continued)

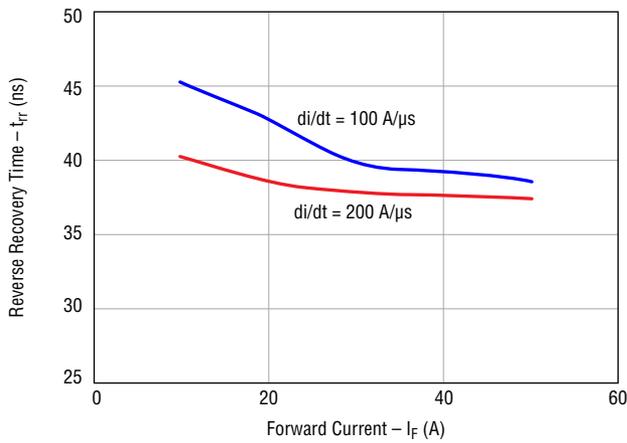
Typical Switching Loss Characteristics vs I_C



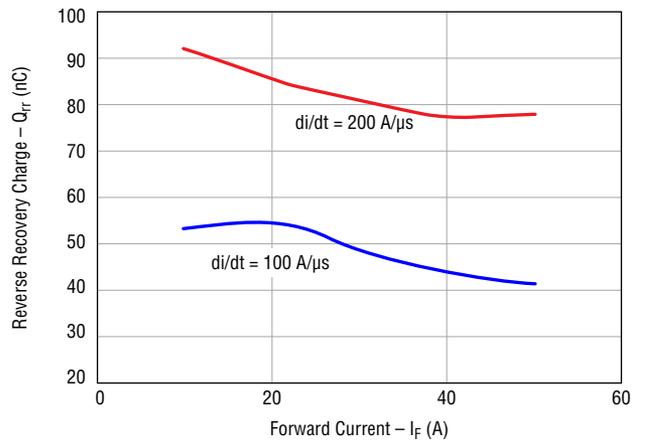
Typical Diode I_F vs V_F



Typical Reverse Recovery Time vs I_F



Typical Reverse Recovery Charge vs I_F



Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

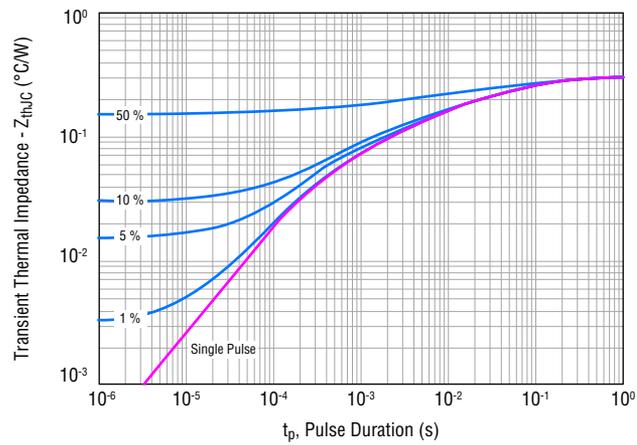
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BIDW50N65T Insulated Gate Bipolar Transistor (IGBT)

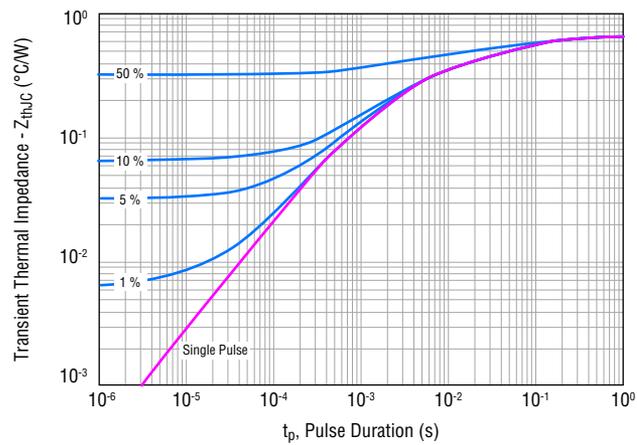


Electrical Characteristic Performance (continued)

IGBT Transient Thermal Impedance vs $t_{p(on)}$ Duration ($D=t_p/T$)



Diode Transient Thermal Impedance vs $t_{p(on)}$ Duration ($D=t_p/T$)



Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

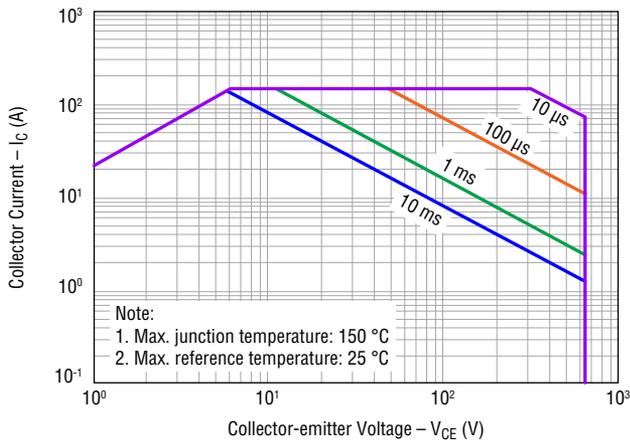
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BIDW50N65T Insulated Gate Bipolar Transistor (IGBT)



Electrical Characteristic Performance (continued)

Forward Bias Safe Operating Area

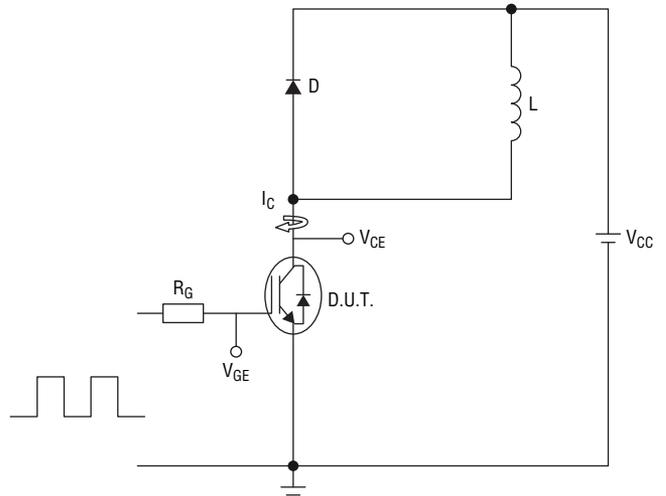


How to Order

B I D W 50 N 65 T

- B = Bourns®
- I = IGBT
- Type
D = Discrete
- Package Code
W = TO-247-3L
- Current Rating
50 = 50 A
- Device Type
N = N-channel
- Nominal Voltage (divided by 10)
65 = 650 V
- Optimization
T = Medium Speed

Inductive Load Test Circuit



$L = 1.12 \text{ mH}$, $V_{CE} = 400 \text{ V}$, $V_{GE} = 15 \text{ V}$, $I_C = 50 \text{ A}$, $R_G = 10 \Omega$

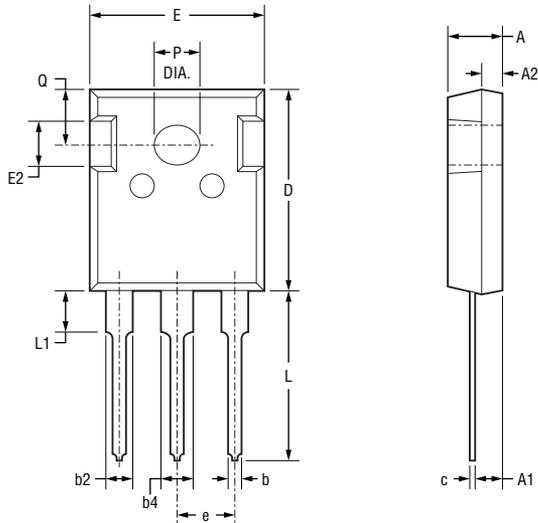
Environmental Characteristics

ESD Class (HBM)2

BIDW50N65T Insulated Gate Bipolar Transistor (IGBT)

BOURNS®

Product Dimensions



DIMENSIONS: $\frac{\text{MM}}{\text{(INCHES)}}$

Symbol	Min.	Nom.	Max.
A	$\frac{4.80}{(.189)}$	$\frac{5.00}{(.197)}$	$\frac{5.20}{(.205)}$
A1	$\frac{2.21}{(.087)}$	$\frac{2.41}{(.095)}$	$\frac{2.59}{(.102)}$
A2	$\frac{1.85}{(.073)}$	$\frac{2.00}{(.079)}$	$\frac{2.15}{(.085)}$
b	$\frac{1.11}{(.044)}$	—	$\frac{1.36}{(.054)}$
b2	$\frac{1.91}{(.075)}$	—	$\frac{2.25}{(.089)}$
b4	$\frac{2.91}{(.115)}$	—	$\frac{3.25}{(.128)}$
c	$\frac{0.51}{(.020)}$	—	$\frac{0.75}{(.030)}$
D	$\frac{20.80}{(.819)}$	$\frac{21.00}{(.827)}$	$\frac{21.30}{(.839)}$
E	$\frac{15.50}{(.610)}$	$\frac{15.80}{(.622)}$	$\frac{16.10}{(.634)}$
E2	$\frac{4.40}{(.173)}$	$\frac{5.00}{(.197)}$	$\frac{5.20}{(.205)}$
e	$\frac{5.44}{(.214)}$ BSC		
L	$\frac{19.72}{(.776)}$	$\frac{19.92}{(.784)}$	$\frac{20.22}{(.796)}$
L1	—	—	$\frac{4.30}{(.169)}$
P	$\frac{3.40}{(.134)}$	—	$\frac{3.80}{(.150)}$
Q	$\frac{5.60}{(.220)}$	$\frac{5.80}{(.228)}$	$\frac{6.00}{(.236)}$

Packaging Specifications

BIDW50N65T 30 pieces per tube

BOURNS®

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EMEA: Tel: +36 88 885 877

Email: eurocus@bourns.com

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Email: americus@bourns.com

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Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

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